Lattice Semiconductor Bridges Interface Gap between Processors and Industrial Displays with New CrossLink Reference Design

June 19, 2019

The MIPI DSI/CSI-2 to OpenLDI LVDS Interface Bridge reference design enables legacy industrial displays to connect to more advanced application processors

HILLSBORO, Ore.--(BUSINESS WIRE)--Jun. 19, 2019-- Lattice Semiconductor Corporation (NASDAQ: LSCC), the low power programmable leader, today announced the availability of the latest in a series of new reference designs featuring the Lattice CrossLink™ FPGA for video bridging applications. The MIPI DSI/CSI-2 to OpenLDI LVDS Interface Bridge reference design provides industrial device customers with a flexible and easy to implement solution to connect today's more advanced application processors (APs) to the legacy displays still used in many of today's industrial environments.

As a result of their long lifespans, industrial devices currently in use were originally designed to connect to displays via legacy interface standards like OpenLDI. However, the APs now favored by many OEMs today require support for the MIPI DSI interface, which isn't backward-compatible with legacy display standards. Rather than redesign their products to support MIPI, one alternative for OEMs is to use a display interface bridging solution. The Lattice CrossLink reference designs lay down the framework required for adding a bridging solution to new and existing product designs.

"With our new CrossLink reference design, industrial OEMs no longer need to waste precious development time towards solving basic interface connectivity issues that don't distinguish their products from the competition," said Peiju Chiang, Product Marketing Manager, Lattice Semiconductor. "Our CrossLink reference designs will give customers a fast, easy and economical way to solve a range of interface design issues so they can focus more of their engineering resources on developing the value-added features that make their products stand out."

Key features of the MIPI DSI/CSI-2 to OpenLDI LVDS Interface Bridge reference design include:

- Support single DSI input (RGB888 or RGB666) to single or dual channel LVDS output (RGB888 or RGB666)
- Support single CSI-2 input (RGB888, RAW8, RAW10, or RAW12) to single- or dual-channel RGB888 LVDS outputs (RGB888)
- Support for MIPI DSI input of up to 1.5 Gbps per lane
- Support for OpenLDI at up to 1.2 Gbps per lane
- Compliant with the MIPI D-PHY v1.1, CSI-2 v1.1, and DSI v1.1 specifications

More information about the new CrossLink MIPI DSI/CSI-2 to OpenLDI LVDS Interface Bridge reference design is available here.

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing communications, computing, industrial, automotive and consumer markets. Our technology, long-standing relationships, and commitment to world-class support lets our customers quickly and easily unleash their innovation to create a smart, secure and connected world.

For more information about Lattice, please visit www.latticesemi.com. You can also follow us via LinkedIn, Twitter, Facebook, YouTube, WeChat, Weibo or Youku.

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