



Lattice FPGA Brings High-Performance MIPI Bridging to Ambarella's CVflow Architecture for Automotive and Machine Vision Applications

August 5, 2020

HILLSBORO, Ore.--(BUSINESS WIRE)--Aug. 5, 2020-- [Lattice Semiconductor Corporation](#) (NASDAQ: LSCC), the low power programmable leader, today announced that Ambarella, Inc. (NASDAQ: AMBA) selected the Lattice ECP5™ FPGA to enable MIPI bridging capabilities in systems using Ambarella CVflow® SoCs. CVflow-based products are used in a wide variety of embedded and smart vision applications, including video security, advanced driver assistance (ADAS), electronic mirror, drive recorder, driver/cabin monitoring, autonomous driving, and robotics. Ambarella chose the Lattice FPGA based on its highly efficient routing architecture, low power consumption, and small form factor.

According to the MIPI Alliance, car manufacturers are interested in using MIPI components because they are “mature, relatively simple to use, and help reduce the number of wires needed to connect components. MIPI specifications are also well-proven, which reduces risk.”¹ However, many automotive and industrial systems require support for legacy interface standards incompatible with MIPI. The Lattice FPGA provides the low power co-processing necessary to convert sub-LVDS and other interface standards supported by legacy components. This allows Ambarella CVflow customers to leverage the latest MIPI devices in their new and existing product designs while maintaining compatibility with other system components.

“Selecting the best components for each of our products is essential to maintaining our industry leadership and ensuring that our users get the rich feature sets they need,” said Mr. Long Xu, Director at Ambarella. “We’re pleased to be working with Lattice to incorporate their low power FPGAs into our SoC platform, which further reinforces Ambarella as a preferred choice for advanced automotive and industrial solutions.”

“Lattice FPGAs and solutions stacks such as mVision enable our customers and partners to quickly implement value-added embedded vision features like MIPI signal bridging and aggregation in their products,” said Kambiz Khalilian, Director, Strategic Business Development, Lattice. “In addition to general-purpose FPGAs, Lattice also offers embedded vision optimized FPGAs such as CrossLink-NX, based on our low power Lattice Nexus platform, that include support for hardened MIPI D-PHY interfaces needed in many industrial and automotive applications.”

For more information about the CVflow chip architecture from Ambarella, please visit <http://www.ambarella.com>.

For more information about the Lattice ECP5 family of low power, general-purpose FPGAs, please visit <http://www.latticesemi.com/ECP5>.

For more information about the CrossLink™-NX family of low power FPGAs for embedded vision, please visit <http://www.latticesemi.com/CrossLink-NX>.

For more information about the Lattice mVision™ solutions stack for embedded vision, please visit <http://www.latticesemi.com/mVision>.

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing communications, computing, industrial, automotive and consumer markets. Our technology, long-standing relationships, and commitment to world-class support lets our customers quickly and easily unleash their innovation to create a smart, secure and connected world.

For more information about Lattice, please visit www.latticesemi.com. You can also follow us via [LinkedIn](#), [WeChat](#), [Weibo](#) or [Youku](#).

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¹ <https://www.mipi.org/automotive>

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