Lattice Semiconductor Enables Faster IEC61508 Certification

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NEWS RELEASE
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Lattice Semiconductor Enables Faster IEC61508 Certification
with FPGA Functional Safety Design Flow

TÜV Rheinland-certified design flow adheres to latest safety design methodology for safety-critical industrial, medical and automotive applications

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- Speed IEC61508 Certification for safety-critical applications
- FPGA Functional Safety Design Flow certified by TÜV Rheinland
- Adhere to latest safety design methodology, save time, reduce costs
- Supports MachXO, MachXO2, LatticeECP3 & other Lattice FPGA families

HILLSBORO, OR - February 24, 2015 - Lattice Semiconductor Corporation (NASDAQ: LSCC), the leader in ultra-low power, small form factor, customizable solutions, today announced the availability of the Functional Safety Design Flowsolution based on the Lattice Diamond® Design Tools. Certified by TÜV-Rheinland, an independent organization globally recognized for safety and quality testing, the package enables users to bring their product to market faster by simplifying and speeding up the IEC61508 safety certification process for applications using Lattice FPGAs.

"By using Lattice's qualified Functional Safety Design Flow, designers can adhere to the latest safety design methodology when developing safety-critical designs, accelerate their certification process and reduce design costs", said Jim Tavacoli, Sr. Director, Product and Segment Marketing, Lattice.

IEC61508 has become the global standard for functional safety certification, and many industry specific standards are derived from it. The Lattice solution comprises a design flow and the development tools necessary to ensure that applications comply up to Safety Integrity level 3 (SIL3) certification. The Functional Safety Design Flow solutionincludes: Lattice Diamond Design Tools suite (a complete design and verification flow including Lattice Synthesis Engine and incorporating third party tools such as Aldec Active-HDL™ simulator and Synopsys Synplify Pro® synthesis) and Safety User Manual. Lattice FPGA families covered include both non-volatile (MachXO™, MachXO2™, LatticeXP2™) and SRAM-base (LatticeECP2™, LatticeECP2M™ & LatticeECP3™) product

About Lattice Semiconductor
Lattice Semiconductor (NASDAQ: LSCC) is the leader in low power, small form factor, low cost, customizable solutions for a quickly changing connected world. From making smart consumer devices smarter, to enabling intelligent industrial automation, or connecting anything to everything in communications, electronics manufacturers around the world use Lattice's solutions for fast time to market, product innovation, and competitive differentiation. For more information, visit www.latticesemi.com. You can also follow us via LinkedIn, Twitter, Facebook or RSS.

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