

Lattice Semiconductor Launches "Lattice Hackathon: Small FPGAs. Big Possibilities" Contest for the Maker Community

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PORTLAND, Ore. & SAN JOSE, Calif.--(BUSINESS WIRE)--Sep. 15, 2016-- <u>Lattice Semiconductor Corporation</u> (NASDAQ:LSCC), the leading provider of customizable smart connectivity solutions, today announced that it is hosting a contest for the Maker Community to develop the next big mobile application. The "Lattice Hackathon: Small FPGAs. Big Possibilities" contest promotes the creation of mobile application devices that leverage Lattice's field-programmable gate array (FPGA) technologies including <u>iCE40™ Mach XO™ ECP5™</u> and <u>CrossLink™</u> product families.

The top three winners will receive a round-trip flight, hotel accommodations and access pass to CES[®] 2017 in Las Vegas. The grand prize winner will also receive \$5,000, as well as an opportunity to demonstrate their solution at Lattice's booth at CES 2017.

"We are very excited to sponsor this contest for the Maker Community," said Abdullah Raouf, product marketing manager at Lattice Semiconductor.

"The Maker Community is very passionate about innovation and we hope Lattice can ignite that passion to spark new, creative and innovative ideas to demonstrate how mobile applications can further advance the next-generation of consumer devices. We look forward to seeing what participants will create with our mobile-friendly FPGA products."

Submissions are now being accepted through Monday, Oct. 17, 2016 at 5:00 p.m. Pacific Time, and will be judged based on uniqueness, functionality, resource utilization and market value. Voting by a panel of Lattice judges will take place Dec. 15, 2016 to select the top three submissions, including the grand prize winner. Participants must be able to attend CES 2017 and be present at Lattice Semiconductor's booth in the Las Vegas Convention Center for one to two hours a day during the show to be eligible to win the Grand Prize of \$5,000 USD. For official contest rules and prize details, please visit www.latticesemi.com/hackathon.

How to Participate:

- Contest Dates Register to qualify by Friday, Sept. 30, 2016 at 5 p.m. Pacific Time. Submissions will be accepted thru Monday, Oct. 17, 2016 at 5 p.m. Pacific Time.
- Requirements Submissions must:
 - Use a Lattice product
 - o Solve a real-world problem targeting mobile (battery powered) consumer products
- Technology Submissions must showcase the benefit of Lattice's FPGA technology for mobile applications
- Criteria Submissions will be judged on the following criteria:
 - Unique solution with competitive edge
 - Impactful demonstration of functionality
 - High device resource utilization
 - o Market value assessment

To enter your submission, or for more details about the contest, please visit the contest website at www.latticesemi.com/hackathon. Be sure to follow #LSCCHackathon for the latest updates on the contest.

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) provides smart connectivity solutions powered by our low power FPGA, video ASSP, 60 GHz millimeter wave, and IP products to the consumer, communications, industrial, computing, and automotive markets worldwide. Our unwavering commitment to our customers enables them to accelerate their innovation, creating an ever better and more connected world.

For more information, visit www.latticesemi.com. You can also follow us via LinkedIn, Twitter, Facebook, YouTube or RSS.

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