SEMICONDUCTOR.

Pedestrian

The Low Power Programmable Leader

Third Quarter, 2019



Safe Harbor

This presentation contains forward-looking statements that involve estimates, assumptions, risks and uncertainties, including statements relating to our expectations about our future systems solutions' capabilities, and the statements under the heading "Q319 Business Outlook."

Factors that may cause actual results to differ materially from the forward-looking statements in this presentation include those risks more fully described in Lattice's filings with the SEC including its annual report on Form 10-K for the fiscal year ended December 29, 2018 and quarterly filings.

You should not unduly rely on forward-looking statements because actual results could differ materially from those expressed in any forward-looking statements. In addition, any forward-looking statement applies only as of the date on which it is made. The Company does not intend to update or revise any forward-looking statements, whether as a result of events or circumstances after the date hereof or to reflect the occurrence of unanticipated events.



Agenda



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The Low Power Programmable Leader

Lattice Semiconductor Overview

APPLICATIONS & MARKETS

We enable secure control, flexible connectivity, and low power compute acceleration

WORLD CLASS SUPPLIER

In business and innovating for 35 years





Lattice Executive Leadership Team



Agenda



Lattice Value Proposition



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Lattice Product Portfolio

BROAD FAMILY OF LOW POWER FPGAs

GENERAL PURPOSE



Addresses a broad range of applications across multiple markets

- Up to 150K LUTs in 10x10 mm csfBGA
- Highest density & lowest power SERDES in smallest package

FPGA FAMILIES TAILORED FOR SPECIFIC NEEDS

VIDEO CONNECTIVITY



Optimized for high speed video and sensor applications

First FPGA with hardened MIPI D-PHY
Highest performance at lowest power

ULTRA LOW POWER



World's lowest power FPGAs; Optimized for small form factor • Sleep current as low as 25uA • World's most popular ultra-low

power FPGA

CONTROL & SECURITY

Machxo Family • 50%

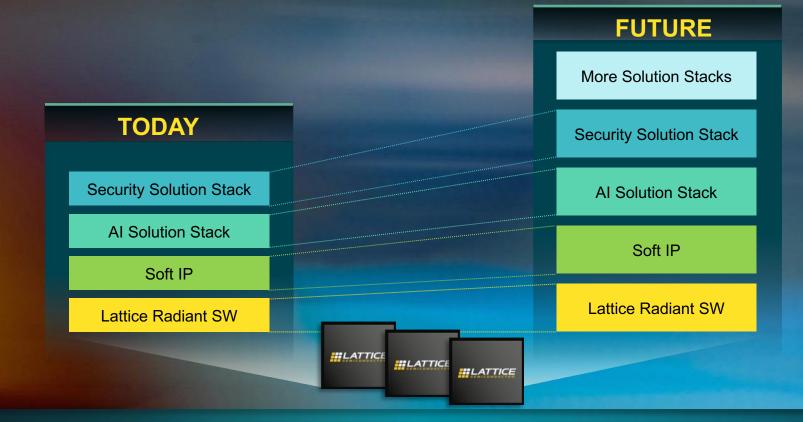
Optimized for platform management & security applications

50% market shareHighest I/O density

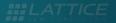




Lattice's Expanded System Solutions



Bringing Additional Value to Our Customers



New Value Added Solutions

sensAl 2.0

New AI Capabilities

High Performance Inferencing Under 1W Lowest power, smallest form factor solutions 10X faster real time image processing

Expanded Machine Learning Capability Quantized training for faster development Keras framework support for broader adoption

Complete Solution Enablement Presence detection, object counting Increased partner ecosystem



Robust Platform Security

Secure Hardware Root of Trust capability First on, last off for maximum security

Designed for NIST Compliance Protect, Detect AND Recover Secures multiple firmware images

Customer Samples Delivered Top server OEMs and Hyperscalers



Advanced Ease-of-use

Simplified Flow for Faster Design Single design database Common timing engine

Increase Re-use With IP Tools Create and distribute IP Use Lattice Supplied IP

Leading Synthesis and Simulation Synopsys Synplify Pro Synthesis Aldec Active HDL Simulation



Agenda



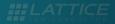


Positioned in Growing End Markets



5G Wireless	Servers	Industrial IoT	ADAS	Smart Home
Switches/Routers	Client	Factory Automation	Infotainment	Wearables

Only Lattice is investing in small, low power FPGAs for these markets



Lattice Solves Communications Challenges

SCALABLE HARDWARE MANAGEMENT

ing the lite of the

ASIC BRIDGING

PCIe BRIDGING

COLUMN

#

SECURE CONTROL

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Lattice Solves Data Center Challenges



PLATFORM FIRMWARE RESILIENCE



SYSTEM CONTROL

COMPUTE ACCELERATION



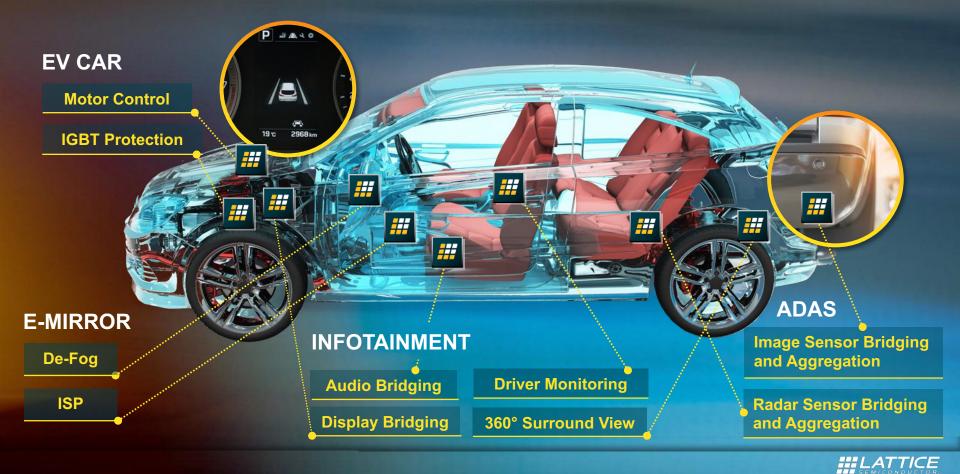
POWER SEQUENCING



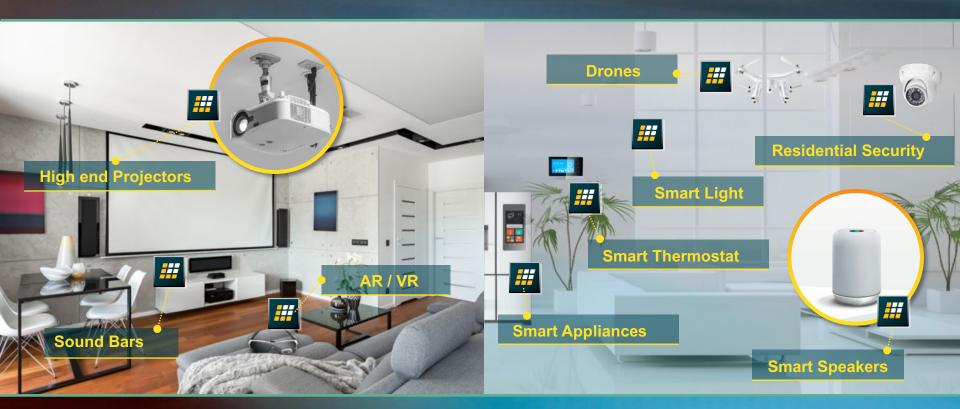




Lattice Solves Automotive Challenges



Lattice Solves Consumer Challenges











Financial Highlights









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