



See Lattice at RISC-V Summit 2018

December 3, 2018

[Lattice Semiconductor Corporation](#) (NASDAQ: LSCC):

Who: [Lattice Semiconductor Corporation](#) (NASDAQ: LSCC), the leading provider of low power, high density, FPGA solutions for control, compute and communications applications.

What: Visit Lattice to learn about ultra-low power implementations of RISC-V processor based on the Lattice iCE40 UltraPlus FPGA. Lattice partner demos include:

- Vector Blox: RISC-V instruction extensions for artificial intelligence (AI).
- MCCI: RISC-V implementation of the LORA communications stack.

When: Tuesday, December 4, 2018, 10:00 am – 7 pm

Wednesday, December 5, 2018, 10:00 am – 3:30 pm

Where: Santa Clara Convention Center, Booth #T14

Santa Clara, CA

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is a leader in smart connectivity solutions at the network edge, where the “things” of IoT live. Our low power FPGA and video ASSP products deliver edge intelligence, edge connectivity, and control solutions to the industrial, consumer, communications, computer, and automotive markets. Our unwavering commitment to our global customers enables them to accelerate their innovation, creating an even better and more connected world.

For more information about Lattice please visit www.latticesemi.com. You can also follow us via [LinkedIn](#), [Twitter](#), [Facebook](#), [YouTube](#), [WeChat](#), [Weibo](#) or [Youku](#).

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