



Lattice Reinvents the Low Power, General-Purpose FPGA with New Certus-NX

June 24, 2020

- *Second Product Released on Lattice Nexus FPGA Platform in Just Six Months*
- *Enables Industry-leading I/O Count in Small Form Factor Packages*

HILLSBORO, Ore.--(BUSINESS WIRE)--Jun. 24, 2020-- [Lattice Semiconductor Corporation](https://www.businesswire.com/news/home/20200624005722/en/) (NASDAQ: LSCC), the low power programmable leader, today launched the new Lattice Certus™-NX family of FPGAs. The devices lead the general-purpose FPGA market in I/O density, delivering up to twice the I/O density per mm² in comparison to similar competing FPGAs, and provide best-in-class power savings, small size, reliability, instant-on performance, and support fast PCI Express (PCIe) and Gigabit Ethernet interfaces to enable data co-processing, signal bridging, and system control. Certus-NX FPGAs target a range of applications, from data processing in automated industrial equipment to system management in communications infrastructure. The Certus-NX devices are the second family of FPGAs developed on the Lattice Nexus™ platform, the industry's first low power FPGA platform using 28 nm FD-SOI process technology. With the launch of Certus-NX, Lattice marks the release of the second device family developed under Lattice's new product development strategy in just six months.

This press release features multimedia. View the full release here: <https://www.businesswire.com/news/home/20200624005722/en/>



The new Certus™NX Low Power, General Purpose FPGA from Lattice Semiconductor (Photo: Business Wire)

"Certus-NX delivers unique and innovative capabilities that set it apart," said Linley Gwennap, Principal Analyst at The Linley Group. "Compared to competing FPGAs of similar gate counts, Lattice offers a much smaller package, greater I/O density, and

lower power."

"Thanks to innovations at the system, architecture, and circuit level in our Lattice Nexus development platform, we are releasing new products at a faster pace and giving our customers more options to choose from as they evaluate the performance and power needs of their applications," said Gordon Hands, Director of Product Marketing, Lattice Semiconductor. "Certus-NX has clearly reset expectations for what developers should look for in general-purpose FPGAs by providing a compelling blend of parallel processing performance and flexible I/O support that enables creative new device designs."

Technology trends like industrial automation and 5G have developers across markets looking to add processing and connectivity to applications that operate at the network Edge. To successfully support these trends, devices require low power processing hardware with support for popular interfaces like PCIe and Gigabit Ethernet. Device developers also need easy-to-use development platforms from a single source that provide the hardware, software, and IP they need to get products to market quickly.

"As an FPGA-centric design house, we're seeing increasing adoption of serial protocols like PCIe and Gigabit Ethernet for chip-to-chip connectivity in many end applications, including 5G and IoT systems," said Sanjeev Kumar, CEO, Logic Fruit Technologies. "The latest generation of FPGAs by Lattice, Certus-NX, supports these standards and provides a high density of I/Os while keeping power consumption and overall device footprint in check. These FPGAs will allow us to rapidly adapt to the challenging and evolving connectivity and performance needs of our customers."

Key features of the Lattice Certus-NX FPGA family include:

- Up to 3x smaller form factor – Certus-NX FPGAs easily fit into compact designs with minimal impact on overall design footprint. For example, Certus-NX FPGAs can enable a complete PCIe solution with a size of only 36 mm². Even when using the smallest package available in the family, Certus-NX FPGAs deliver up to twice the I/O density per mm² in comparison to similar competing FPGAs to enable greater design flexibility and robust support for bridging applications.
- Robust I/O interfacing capability – Certus-NX developers will have access to Lattice's extensive IP library. Notable IP blocks available on Certus-NX include:
 - 1.5 Gbps differential I/O with performance that is up to 70 percent higher than competing FPGAs.
 - 5 Gbps PCIe, 1.5 Gbps SGMII, and 1066 Mbps DDR3. Developers can test these IP blocks on Certus-NX development boards for the fast implementation of interfaces commonly used in Certus-NX FPGAs' target applications.
- Robust authentication and encryption – To secure the devices' bitstream against unauthorized access/alteration/copying, Certus-NX FPGAs support AES-256 with ECDSA authentication to protect devices throughout their lifecycles. Currently, Certus-NX devices are the smallest FPGAs on the market to support ECDSA.

- Up to 4x lower power consumption – Certus-NX FPGAs use a programmable back bias to enable user-selectable high performance or low power operating modes, depending on the needs of their application. The devices can provide power consumption up to 4x lower than similar FPGAs.
- Instant-on performance – The devices also offer ultra-fast device configuration from SPI memory that is up to 12x faster than similar competing FPGAs, with individual I/Os able to configure in just 3 ms, and full-device startup in only 8-14 ms depending on device capacity.
- High reliability – Reliable performance is key for safety-critical industrial and automotive applications where devices must perform as expected to avoid damage/injury. Certus-NX FPGAs offer up to 100 times better soft-error rate (SER) performance than similar FPGAs. Certus-NX FPGAs are temperature-rated for use in industrial applications. They also support ECC and SEC in hardware.
- Ease of use/design – Certus-NX FPGAs support Lattice Radiant® software, an easy-to-use, unified FPGA design environment that integrates best-in-class tools and features to help users develop their applications quickly and efficiently.

Lattice has already shipped sample devices to select customers. For more information, please visit <http://www.latticesemi.com/Certus-NX>.

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing communications, computing, industrial, automotive, and consumer markets. Our technology, long-standing relationships, and commitment to world-class support lets our customers quickly and easily unleash their innovation to create a smart, secure and connected world.

For more information about Lattice, please visit www.latticesemi.com. You can also follow us via [LinkedIn](#), [Twitter](#), [Facebook](#), [YouTube](#), [WeChat](#), [Weibo](#) or [Youku](#).

Lattice Semiconductor Corporation, Lattice Semiconductor (& design) and specific product designations are either registered trademarks or trademarks of Lattice Semiconductor Corporation or its subsidiaries in the United States and/or other countries. The use of the word “partner” does not imply a legal partnership between Lattice and any other entity.

GENERAL NOTICE: Other product names used in this publication are for identification purposes only and may be trademarks of their respective holders.

View source version on [businesswire.com](https://www.businesswire.com/news/home/20200624005722/en/): <https://www.businesswire.com/news/home/20200624005722/en/>

MEDIA CONTACT:

Bob Nelson
Lattice Semiconductor
408-826-6339
Bob.Nelson@latticesemi.com

INVESTOR CONTACT:

Rick Muscha
Lattice Semiconductor
408-826-6000
Rick.Muscha@latticesemi.com

Source: Lattice Semiconductor Corporation