

Lattice Launches 2nd Generation Security Solution with New Mach-NX FPGA for Next Generation, Cyber-Resilient Systems

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- Adds Secure Enclave with Support for ECC 384 and SPDM Protocols, Increases System Control Customization Capabilities
- Enables Hardware Root-of-Trust, PFR, and End-to-End Supply Chain Security Across Multiple Applications, Including Latest Industry-Standard Server Platforms

HILLSBORO, Ore.--(BUSINESS WIRE)--Dec. 8, 2020-- Lattice Semiconductor Corporation (NASDAQ: LSCC), the low power programmable leader, today announced the Lattice Mach™-NXFPGA family, the second generation in its successful line of secure control FPGAs. Building on the capabilities of the Lattice MachXO3D™ family announced in 2019, Mach-NX FPGAs deliver heightened security features and the fast, power-efficient processing needed to implement a real-time Hardware Root-of-Trust (HRoT) on future server platforms, as well as computing, communications, industrial, and automotive systems. Mach-NX marks the third FPGA family developed on the Lattice Nexus™FPGA platform in a year.

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The Lattice Mach-NX secure control FPGA (Photo: Business Wire)

integration, and throughout the product's operational lifetime."

"The race is on between bad actors trying to exploit firmware vulnerabilities and developers designing server platforms with the security features and performance to stop them," said Patrick Moorhead, president and founder of Moor Insights & Strategy. "Protecting systems better requires a real-time HRoT with support for stronger cryptography algorithms like ECC 384 and new, robust data security protocols like SPDM. I believe technologies like Lattice's Mach FPGA families can simplify and accelerate implementation of these technologies for server OEMs looking to better secure their platforms against cyberattack and IP theft."

Esam Elashmawi, Chief Strategy and Marketing Officer at Lattice, added: "Securing systems against unauthorized firmware access goes beyond establishing a HRoT at boot. It also requires that components used to build the system are not compromised as they move through the global supply chain. When combined with the additional protection afforded by our SupplyGuard security service, Lattice Mach-NX FPGAs can protect a system throughout its entire lifecycle: beginning at the time components start moving through the supply chain, through initial product assembly, end-product shipping,

Building on the system control capabilities of the Mach family, Mach-NX FPGAs combine a secure enclave (an advanced, 384-bit hardware-based crypto engine supporting reprogrammable bitstream protection) with a logic cell (LC) and I/O block. The secure enclave helps secure firmware, and the LC and I/O block enable system control functions such as power management and fan control. Mach-NX FPGAs can verify and install the over-the-air firmware updates that keep systems compliant with evolving security guidelines and protocols. The Mach-NX FPGAs parallel processing architecture and dual-boot flash memory configuration provide the near instantaneous response times needed to detect and recover from attacks (a level of performance beyond the capabilities of other HRoT platforms like MCUs). Mach-NX FPGAs will support the Lattice SentryTM solutions stack, a

robust combination of customizable embedded software, reference designs, IP, and development tools to accelerate the implementation of secure systems compliant with NIST Platform Firmware Resiliency (PFR) Guidelines (NIST SP-800-193).

Key features of the Mach-NX family include:

- Secure system control Mach-NX FPGAs' logic (up to 11K LCs) and high I/O count (up to 379) enable fast and secure
 system control. Lattice is a long-standing leader in programmable logic for system control. Mach FPGAs have an attach
 rate of over 80 percent on current shipping server platforms.
- Robust standards and protocol compliance the Mach-NX FPGAs' 384-bit hardware crypto engine supports quickand-easy implementation of leading-edge cryptography like ECC 384 and industry-standard security protocols such as NIST SP-800-193 and MCTP-SPDM. Upcoming server platforms will require support for these protocols.
- End-to-end supply chain protection Mach-NX FPGAs are supported by the <u>Lattice SupplyGuard™</u> supply chain security subscription service. SupplyGuard gives OEMs and ODMs peace-of-mind by tracking locked Lattice FPGAs through their entire lifecycle, from the point of manufacture, through transport via the global supply chain, system integration and assembly, initial configuration, and deployment.
- Rapidly customizable the <u>Lattice Propel™</u> design environment accelerates design of a customized, PFR-compliant HRoT solution. The tool uses a GUI-based development environment that allows developers to create PFR solutions while minimizing the need to write RTL code.

For More Information

To learn more about the Lattice technologies mentioned above, please visit:

- www.latticesemi.com/Mach-NX
- www.latticesemi.com/LatticeNexus
- www.latticesemi.com/MachXO3D
- www.latticesemi.com/LatticeSentry
- · www.latticesemi.com/LatticeSupplyGuard
- www.latticesemi.com/LatticePropel

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing communications, computing, industrial, automotive, and consumer markets. Our technology, long-standing relationships, and commitment to world-class support lets our customers quickly and easily unleash their innovation to create a smart, secure and connected world.

For more information about Lattice, please visit <u>www.latticesemi.com</u>. You can also follow us via <u>LinkedIn</u>, <u>Twitter</u>, <u>Facebook</u>, <u>YouTube</u>, <u>WeChat</u>, <u>Weibo</u> or <u>Youku</u>.

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