Lattice Expands Low Power, Small FPGA Portfolio with High I/O Density and Secure Device Options

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—Doubles its general purpose, secure control device options with new Certus-NX and MachXO5-NX FPGA devices —

HILLSBORO, Ore.--(BUSINESS WIRE)--Jul. 15, 2025-- Lattice Semiconductor (NASDAQ: LSCC), the low power programmable leader, today announced that the company expanded its small FPGA portfolio with new, high I/O density options in the Lattice Certus™-NX FPGA and Lattice MachXO5™-NX FPGA device families. The new offerings include multiple logic density and I/O options in a variety of new packages. These new devices are ideal for a wide range of solutions that require low power consumption, a small form factor, high 3.3V I/O, and security capabilities —making them well-suited for power-constrained AI, Industrial, Communications, Server, and Automotive applications.

“At Lattice, we’re committed to empowering our customers with the flexibility and features they need to innovate across a wide range of applications,” said Dan Mansur, Corporate Vice President, Product Marketing at Lattice Semiconductor. “By introducing higher I/O density and additional security-focused configurations in compact, low power devices, we’re enabling developers with new ways to address the stringent thermal, connectivity, and form factor constraints of their designs.”

Built on the award-winning Lattice Nexus™ FPGA platform, the newly added Lattice Certus-NX and MachXO5-NX devices offer the following features in comparison to competing FPGAs of a similar class in market:

- **Robust and Customizable Connectivity in Small Form Factor**
  - Highest I/O count per package, including 3.3V I/O with up to 2X more I/O per package body size
  - Differential I/O (1.5 Gbps) enabling up to 70% faster data transfer
- **Industry-leading System Integration Capabilities**
  - Up to 4X lower power enabling simplified thermal management
  - Fast time to market and cost efficiency through no power sequencing order requirements
  - Integrated flash options for enhanced secure configuration
  - Up to 12X faster configuration performance for ultrafast boot time
- **Highest Reliability and Device Security**
  - Up to 100X lower soft error rate, improving system reliability for safety-critical applications
  - Built-in SEC and memory block ECC for SEU protection

The new Certus-NX and MachXO5-NX FPGA devices are shipping today and are supported by the latest release of Lattice Radiant™ design software.

For more information about the technologies mentioned above, please visit:

- Lattice Certus-NX FPGA Family
- Lattice MachXO5-NX FPGA Family
- Lattice Nexus Platform
- Lattice Radiant Software

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing Communications, Computing, Industrial, Automotive, and Consumer markets. Our technology, long-standing relationships, and commitment to world-class support let our customers quickly and easily unleash their innovation to create a smart, secure, and connected world.

For more information about Lattice, please visit www.latticesemi.com. You can also follow us via LinkedIn, X, Facebook, YouTube, WeChat, or Weibo.

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MEDIA CONTACT:
Sophia Hong
Lattice Semiconductor
503-268-8786
Sophia.Hong@latticesemi.com

INVESTOR CONTACT:
Rick Muscha
Lattice Semiconductor
408-826-6000
Rick.Muscha@latticesemi.com

Source: Lattice Semiconductor