LatticeXP2 FPGAs Power the First Low Power, Real-Time Video Processing Engine

March 15, 2011

HILLSBORO, OR, Mar 15, 2011 (MARKETWIRE via COMTEX) -- Lattice Semiconductor Corporation (NASDAQ: LSCC) today announced that Thruput Ltd has chosen its LatticeXP2(TM) FPGAs for use in MIDAS, the first low-power, lossless real-time video processing platform. Boasting an impressive eight dual-link inputs and driving up to three full resolution monitors, the Thruput MIDAS delivers over two billion pixels per second of silent processing power. The MIDAS toolbox enables users to process outputs at an incredible 48 ms average latency, enabling real-time video cropping, translation, alpha channel layering and color manipulation.

"The Thruput MIDAS demonstrates our capabilities in the professional video processing market," said Mike Clery, Managing Director of Thruput. "MIDAS has proven to be an elegant solution for a great many video processing applications. We chose the LatticeXP2 FPGA primarily because its low power did not compromise the performance demands of MIDAS."

The MIDAS processing is implemented using 17 highly integrated LatticeXP2 FPGAs. Video is processed through a high-speed, exclusive video hardware data path that provides a deterministic latency and controlled failure modes, allowing MIDAS to be used in the most demanding of safety critical systems. The MIDAS video outputs use the LatticeXP2 FPGAs to provide precise timing and full synchronization, making the MIDAS ideal for ultra-high video output resolutions such as oversized flat-panel displays and next-generation video projectors.

In addition to the wide range of applications that already benefit from the Thruput MIDAS technology, MIDAS has recently been adopted as a real-time processing offload engine for vehicle simulators. The MIDAS alpha-blending and color manipulation features are perfect for combining video sources, and provide a novel solution for the removal of radar clutter-zones caused by wind farm interference.

"Our LatticeXP2 FPGA family has proven to be an exceptional design solution for video systems and applications," said Niladri Roy, Senior Manager of product marketing at Lattice Semiconductor. "We are excited to be working with Thruput Ltd and pleased that our LatticeXP2 FPGAs are helping to implement its advanced video processing engine."

About the LatticeXP2 FPGA Family The LatticeXP2 family combines a Look-up Table (LUT)-based FPGA fabric with Flash non-volatile cells, providing the industry's smallest form factor non-volatile SRAM-based FPGAs. The flexiFLASH(TM) approach provides benefits such as instant-on operation, a small footprint, on-chip storage with embedded block memories, serial TAG memory and the highest design security. LatticeXP2 devices also support live updates with Lattice's unique TransFR(TM) technology, 128-bit AES design encryption and dual-boot technologies. The family includes five devices, ranging between 5K and 40K LUTs, in a wide variety of packages. All LatticeXP2 FPGAs are fully production qualified and have been shipping for three years.

About Thruput Thruput is dedicated to the creation of unique, and innovative, high-end graphics processing solutions. Thruput systems are selected worldwide for their reliability, integrity and performance. For more information, visit thruput.co.uk. Follow Thruput via Facebook, RSS and Twitter.

About Lattice Semiconductor Lattice is the source for innovative FPGA, PLD, programmable Power Management and Clock Management solutions. For more information, visit www.latticesemi.com. Follow Lattice via Facebook, RSS and Twitter.

EDITORIAL/READER CONTACT:
Brian Kiernan
Corporate Communications Manager
Lattice Semiconductor Corporation
503-268-8739 voice
503-268-8193 fax
brian.kiernan@latticesemi.com

SOURCE: Lattice Semiconductor Corporation