



Lattice to Showcase New Products, Technologies and Applications at RTS Embedded Show

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HILLSBORO, OR, Mar 23, 2011 (MARKETWIRE via COMTEX) -- Lattice Semiconductor Corporation (NASDAQ: LSCC) today announced its plans for the RTS Embedded show, part of Salons Solutions Electroniques, which will take place March 29-31 at Porte de Versailles, Paris, Hall 8. The Lattice exhibit, Stand D26, will feature several new products, including the MachXO2(TM) PLD Family and Platform Manager(TM) devices, as well as the award-winning LatticeECP3(TM) FPGA family. The recently announced LatticeECP3-based High definition (HD) Video Camera Development Kit with High Dynamic Range (HDR) also will be demonstrated.

MachXO2 PLD Family Among the new products that Lattice will display is its recently introduced MachXO2 family of Programmable Logic Devices (PLDs). The MachXO2 devices provide embedded designers an unprecedented mix of low cost, low power and high system integration. The MachXO2 family delivers a 3X increase in logic density, a 10X increase in embedded memory, more than a 100X reduction in static power and up to 30% lower cost compared to the prior generation MachXO PLD family. These devices are ideally suited for the most popular functions used in embedded system applications (telecom infrastructure, computing, high end industrial, high end medical) and embedded consumer applications (digital TVs, smart phones, GPS devices, mobile computing, digital cameras).

Platform Manager Family: "Product of the Year" In designing highly integrated circuit boards that contain complex devices such as ASICs, microprocessors and FPGAs, digital and power supply management is critical. Lattice recently introduced the programmable Platform Manager family, named "2010 Product of the Year" by Electronic Products Magazine, which called the Platform Manager family "a leap in the thought process when designing power and digital management functions."

The Platform Manager family significantly simplifies board management design by integrating programmable analog and digital logic to support many common functions such as power management, digital housekeeping and glue logic. By integrating all of these support functions the Platform Manager device not only reduces design cost compared to traditional approaches, but also improves system reliability and provides a high degree of design flexibility that minimizes the risk of circuit board re-spins.

LatticeECP3 FPGA Family Also on display will be the award-winning LatticeECP3 FPGA family, which offers multi-protocol 3.2G SERDES with XAUI jitter compliance, DDR3 memory interfaces, powerful DSP capabilities, high density on-chip memory and up to 149K LUTS, all with half the power consumption and half the price of competitive SERDES-capable FPGAs.

HDR-60 Video Camera Development Kit A LatticeECP3-based High definition (HD) Video Camera Development Kit with High Dynamic Range (HDR) will be demonstrated. The Kit is a production-ready HD video camera with complete Image Signal Processing (ISP) pipeline from Lattice partner Helion GmbH. Implemented in stream-processing mode through the LatticeECP3 FPGA, the end-to-end ISP pipeline, from sensor to HDMI/DVI output, needs no external frame buffer memory, is full HD 1080p60 capable and features the industry's fastest auto-exposure and 120+dB High Dynamic Range and Auto White Balance. With a production-ready reference design and free schematics and layout files, the kit is designed to jumpstart video camera manufacturers' FPGA-based camera initiatives. The kit provides ease of programming via standard USB cable and support for legacy cable networks, with the ability to support Ethernet over coax up to a distance of 700 meters. The IP is scalable easily up to 16 megapixel sensors, making the kit virtually future proof.

CCTV System Multi-Camera Processor Also featured will be a multi-camera processor CCTV system developed by Lattice partner Tachyssema. One or more cameras can be connected to this system without any additional work for the user. The video data are written in the external memory and the user can adapt the ISP pipeline to meet specific requirements. The control display and the camera selection are done through a USB port. This CCTV system uses the LatticeXP2(TM) non-volatile FPGA, a single-chip design solution that features instant-on capability and the ability to implement live updates.

About Lattice Semiconductor Lattice is the source for innovative FPGA, PLD, programmable Power Management and Clock Management solutions. For more information, visit www.latticesemi.com. Follow Lattice via Facebook, RSS and Twitter.

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EDITORIAL/READER CONTACT:
Brian Kiernan
Corporate Communications Manager
Lattice Semiconductor Corporation
503-268-8739 voice

503-268-8193 fax
brian.kiernan@latticesemi.com

SOURCE: Lattice Semiconductor Corporation