



Lattice Semiconductor Announces MachXO3LF Devices With On-Chip Flash Memory

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NEWS RELEASE

For Further Information:

Sherrie Gutierrez
Lattice Semiconductor
408-616-4017
sherrie.gutierrez@latticesemi.com

Ellie O'Rourke
Mobility Public Relations
425.233.2092
latticesemi@mobilitypr.com

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The newest generation of Lattice's non-volatile, instant-on MachXO3 FPGA line provides the most advanced, low-cost-per I/O bridging and I/O expansion solution now shipping

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- The MachXO3LF device adds on-chip Flash memory.
- With the MachXO3 family, customers now have multiple footprint compatible options: the MachXO3L device, which offers low-cost reprogrammable non-volatile configuration memory (NVCM), and now the MachXO3LF device with Flash memory

PORTLAND, OR - May 11, 2015 - Lattice Semiconductor Corporation (NASDAQ: LSCC), the leading provider of customizable smart connectivity solutions, today announced the MachXO3LF™ device, the newest member of its MachXO3™ FPGA family, which provides essential bridging and I/O expansion functions to meet the increasing connectivity requirements of communications, computing, consumer and industrial markets.

MachXO3LF devices come with on-chip Flash for configuration and usage. They utilize the same advanced package technology as the MachXO3L™ family, thus providing a small-footprint and the highest I/O density of any device on the market. The pin-out compatibility allows customers to easily migrate between MachXO3L and MachXO3LF devices without changing printed circuit board design. With the MachXO3LF devices, customers can get the benefit of FPGA design code changes during the engineering and development phase or during in-field design upgrades, and then easily migrate to lower cost MachXO3L devices when the design is finalized or an upgrade becomes unnecessary. The new MachXO3LF devices are immediately supported by Lattice Diamond® design software version 3.4.1 (and follow-on versions).

The MachXO3 FPGA family of devices is a 2014 [EDN Hot 100](#) Winner and provides the ability for manufacturers to save costs with a variety of solutions, including MIPI® CSI-2 Image Sensor Interfacing, DSI LCD Display Interfacing, Microprocessor Interface Expansion and system power sequencing, as well as performing numerous control functions.

"The MachXO3 family has been tremendously received by both industry observers, and more importantly, the customers who use it to differentiate their products," said Jim Tavecchi, senior director of marketing at Lattice Semiconductor. "The MachXO3LF device continues the evolution of the product line by providing more options for customers."

The MachXO3 FPGA family is widely successful in mobile, consumer, industrial, computation and communication applications. It has the lowest-cost per I/O, a small footprint, low power usage, high I/O count, high I/O density, low cost NVCM or Flash options for programming and configuration, driving the MachXO3 FPGA to be customer's default choice for CPLD and low density FPGA devices.

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the global leader in smart connectivity solutions, providing market leading intellectual property and low-power, small form-factor devices that enable more than 8,000 global customers to quickly deliver innovative and differentiated cost and power efficient products. The Company's broad end-market exposure extends from consumer electronics to industrial equipment, communications infrastructure and licensing.

Lattice was founded in 1983 and is headquartered in Portland, Oregon. In March 2015, the Company acquired Silicon Image, which is a leader in setting industry standards including the highly successful HDMI®, DVI™, MHL® and WirelessHD® standards.

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