



## Lattice Semiconductor Enables Rapid Prototyping of Smart Connectivity Designs with ECP5 Versa Development Kit

September 1, 2015

**Promotional Offer Now Available for New ECP5™ Versa Development Kit and Lattice Diamond® Software**

[Click-to-Tweet](#)

- ECP5 Versa development kit and Lattice Diamond software promotional offer now available for \$99 for a limited time
- ECP5 family offers low power consumption, small form factor and low cost FPGAs for flexible connectivity to ASICs and ASSPs
- ECP5 devices are targeted for small cells, microservers, broadband access, and industrial video

PORTLAND, Ore.--(BUSINESS WIRE)--Sep. 1, 2015-- Lattice Semiconductor Corporation (NASDAQ:LSCC), the leading provider of customizable smart connectivity solutions, today announced the availability of the [ECP5™ Versa development kit](#) to accelerate prototyping and testing of connectivity designs targeted for small cells, microserver, broadband access, and industrial video applications worldwide. The family's low power consumption, small form factor and low cost make it an ideal connectivity solution and enables design engineers to rapidly add features and functions that complement those delivered by ASICs and ASSPs, reducing development risk and accelerating time-to-market.

To help customers interested in the ECP5 device family to quickly design and test application prototypes, Lattice has released an ECP5 Versa development kit. The kit lets customers evaluate the connectivity performance of the ECP5 FPGA with a range of standards, including PCI Express, Gigabit Ethernet, DDR3 and generic SERDES. Lattice also offers several proof-of-concept demos with the ECP5 Versa development kit that can help customers accelerate prototyping and testing of their designs.

Additionally, the ECP5 Versa development kit includes the Lattice Diamond Design Software that offers a complete suite of FPGA design tools with an easy-to-use interface, efficient design flow, superior design exploration and more. The Lattice Diamond software suite tailored specifically for the ECP5 Versa kit will be available free of charge to all users purchasing the board.

The new ECP5 Versa development kit is available direct from Lattice at a limited promotional price of \$99 (US). To order the kit or learn more about the kit and its demo and design software, please visit the [ECP5 Versa development kit web page](#).

"When we developed the ECP5 family, we broke all the rules of conventional FPGA approaches in order to deliver the optimum connectivity solution tailored to meet the demands of compact, low power, high volume communications and industrial applications," said Deepak Boppana, director, product marketing at Lattice Semiconductor. "ECP5 devices have proven to be an ideal companion chip for ASICs and ASSPs and we believe that the new ECP5 Versa development board will only help to further increase broad adoption across our served markets."

Lattice optimized the ECP5 family's architecture with the goal of delivering the best value below 100K LUTs, while adding key new features such as support for soft error correction, and small form factor packages across all densities. Achieving 40% lower cost than competing solutions, optimizations include small LUT4 based logic slices with enhanced routing architecture, dual-channel SERDES to save silicon real estate, and enhanced DSP blocks for up to 4x resource improvements.

### About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the global leader in smart connectivity solutions, providing market leading intellectual property and low-power, small form-factor devices that enable more than 8,000 global customers to quickly deliver innovative and differentiated cost and power efficient products. The Company's broad end-market exposure extends from consumer electronics to industrial equipment, communications infrastructure and licensing.

Lattice was founded in 1983 and is headquartered in Portland, Oregon. In March 2015, the Company acquired Silicon Image, which is a leader in setting industry standards including the highly successful HDMI®, DVI™, MHL® and WirelessHD® standards.

For more information, visit [www.latticesemi.com](http://www.latticesemi.com). You can also follow us via [LinkedIn](#), [Twitter](#), [Facebook](#), or [RSS](#).

Lattice Semiconductor Corporation, Lattice Semiconductor (& design), L (& design), DVI, ECP5, HDMI, Lattice Diamond, MHL, WirelessHD, and specific product designations are either registered trademarks or trademarks of Lattice Semiconductor Corporation or its subsidiaries in the United States and/or other countries.

**GENERAL NOTICE:** Other product names used in this publication are for identification purposes only and may be trademarks of their respective holders.

View source version on businesswire.com: <http://www.businesswire.com/news/home/20150901005295/en/>

Source: Lattice Semiconductor Corporation

**MEDIA CONTACTS:**

Lattice Semiconductor

Sherrie Gutierrez, 408-616-4017

[sherrie.gutierrez@latticesemi.com](mailto:sherrie.gutierrez@latticesemi.com)

or

Voce Communications

Bob Nelson, 408-738-7889

[LatticeTeam@vocecomm.com](mailto:LatticeTeam@vocecomm.com)

or

**INVESTOR CONTACT:**

Global IR Partners

David Pasquale, 914-337-8801

[lscc@globalirpartners.com](mailto:lscc@globalirpartners.com)