UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, DC 20549

FORM 8-K

CURRENT REPORT
Pursuant to Section 13 or 15(d) of
The Securities Exchange Act of 1934

Date of Report (Date of earliest event reported)
June 5, 2009

Lattice Semiconductor Corporation

(Exact name of registrant as specified in its charter)

Delaware (State or other jurisdiction of incorporation) 000-18032 (Commission File Number) 93-0835214 (IRS Employer Identification No.)

5555 N. E. Moore Court Hillsboro, Oregon 97124-6421 (Address of principal executive offices, including zip code)

 $\begin{tabular}{ll} (503)\ 268-8000 \\ (Registrant's\ telephone\ number,\ including\ area\ code) \\ \end{tabular}$

(Former name or former address, if changed since last report)

Check the appropriate box below if the Form 8-K filing is intended to simultaneously satisfy the filing obligation of the registrant under any of the following provisions (see General Instruction A.2. below):

Written communications pursuant to Rule 425 under the Securities Act (17 CFR 230.425)

Soliciting material pursuant to Rule 14a-12 under the Exchange Act (17 CFR 240.14a-12)

Pre-commencement communications pursuant to Rule 14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b))

Pre-commencement communications pursuant to Rule 13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c))

Item 7.01. Regulation FD Disclosure.

On June 5, 2009, Lattice Semiconductor Corporation issued a press release announcing a business update for the second quarter of fiscal year 2009. A copy of the press release is furnished as Exhibit 99.1 to this report.

The information in this Item 7.01 and in Exhibit 99.1 shall not be deemed to be "filed" for the purposes of Section 18 of the Securities Exchange Act of 1934, as amended (the "Exchange Act"), or incorporated by reference in any filing under the Securities Act of 1933, as amended, or the Exchange Act, except as shall be expressly set forth by specific reference in such filing.

Item 9.01. Financial Statements and Exhibits.

(d) Exhibits.

Exhibit No.	Description
99.1	Press Release dated June 5, 2009 (furnished, not filed, herewith)

SIGNATURES

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

LATTICE SEMICONDUCTOR CORPORATION

By: /s/ Michael G. Potter

Michael G. Potter

Corporate Vice President and Chief Financial Officer

Date: June 5, 2009

EXHIBIT INDEX

Exhibit No. Description

99.1 Press Release dated June 5, 2009 (furnished, not filed, herewith)



For more information contact:

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David Pasquale Global IR Partners 914-337-8801 lscc@globalirpartners.com

LATTICE SEMICONDUCTOR UPDATES GUIDANCE FOR SECOND QUARTER

HILLSBORO, OR - June 5, 2009 - Lattice Semiconductor (NASDAQ: LSCC) today announced updated guidance for the second quarter ending July 4, 2009.

• Second quarter revenue is now expected to be approximately flat to up 5% sequentially. This upward revision compares to previous guidance that the second quarter revenue would be plus or minus 5% on a sequential basis.

No conference call will be held in conjunction with this guidance update. Additional information related to the second quarter will be available when the Company reports its second quarter 2009 results.

Forward-Looking Statements

The foregoing business update contains forward-looking statements. Lattice believes the factors identified below in connection with each such statement could cause actual results to differ materially from the forward-looking statements.

Estimates of future revenue are inherently uncertain due to the high percentage of quarterly "turns" business, as well as such factors as the demand for our products, and our ability to supply products to customers in a timely manner.

In addition to the foregoing, other factors that may cause actual results to differ materially from the forward-looking statements herein include global economic uncertainty, overall semiconductor market conditions, market acceptance and demand for our new products, the Company's transition to new distributors in various regions including Taiwan and China, our dependencies on our silicon wafer suppliers, the impact of competitive products and pricing, technological and product development risks, the compromised liquidity of our auction rate securities, the transition to a new executive management team, and the other risks that are described herein and that are otherwise described from time to time in our filings with the Securities and Exchange Commission. The Company does not intend to update or revise any forward-looking statements, whether as a result of events or circumstances after the date hereof or to reflect the occurrence of unanticipated events.

About Lattice Semiconductor

Lattice is the source for innovative FPGA, CPLD and Mixed Signal programmable logic solutions. For more information, visit www.latticesemi.com.

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