
UNITED STATES
SECURITIES AND EXCHANGE COMMISSION
Washington, DC 20549

FORM 8-K

CURRENT REPORT
Pursuant to Section 13 or 15(d) of
The Securities Exchange Act of 1934

Date of Report (Date of earliest event reported)
September 14, 2011

Lattice Semiconductor Corporation

(Exact name of registrant as specified in its charter)

Delaware
(State or other jurisdiction of
incorporation)

000-18032
(Commission File Number)

93-0835214
(IRS Employer
Identification No.)

5555 N. E. Moore Court
Hillsboro, Oregon 97124-6421
(Address of principal executive offices, including zip code)

(503) 268-8000
(Registrant's telephone number, including area code)

(Former name or former address, if changed since last report)

Check the appropriate box below if the Form 8-K filing is intended to simultaneously satisfy the filing obligation of the registrant under any of the following provisions (see General Instruction A.2. below):

- Written communications pursuant to Rule 425 under the Securities Act (17 CFR 230.425)
- Soliciting material pursuant to Rule 14a-12 under the Exchange Act (17 CFR 240.14a-12)
- Pre-commencement communications pursuant to Rule 14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b))
- Pre-commencement communications pursuant to Rule 13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c))

Item 7.01. Regulation FD Disclosure.

On September 14, 2011, Lattice Semiconductor Corporation issued a press release announcing a business update for the third quarter of fiscal year 2011. A copy of the press release is furnished as Exhibit 99.1 to this report.

The information in this Item 7.01 and in Exhibit 99.1 shall not be deemed to be “filed” for the purposes of Section 18 of the Securities Exchange Act of 1934, as amended (the “Exchange Act”), or incorporated by reference in any filing under the Securities Act of 1933, as amended, or the Exchange Act, except as shall be expressly set forth by specific reference in such filing.

Item 9.01. Financial Statements and Exhibits.

(d) Exhibits.

<u>Exhibit No.</u>	<u>Description</u>
99.1	Press Release dated September 14, 2011 (furnished, not filed, herewith)

SIGNATURES

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

LATTICE SEMICONDUCTOR CORPORATION

By: /s/ Joe Bedewi

Joe Bedewi

Corporate Vice President and Chief Financial Officer

Date: September 14, 2011

EXHIBIT INDEX

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News Release

For more information contact:

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Lattice Semiconductor Corporation
503-268-8000

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LATTICE SEMICONDUCTOR UPDATES 3Q11 REVENUE GUIDANCE

HILLSBORO, OR - September 14, 2011 - Lattice Semiconductor (NASDAQ: LSCC) today updated its revenue guidance for the third quarter ending October 1, 2011.

- Third quarter revenue is now expected to be flat to down 4% on a sequential basis. This compares to prior guidance provided on July 21, 2011 that revenue was expected to be flat to up 5% on a sequential basis. The revised guidance reflects a recent softening of demand primarily in the communications business.
- Expectations for both gross margin percentage and total operating expenses are unchanged from prior guidance, at approximately 60% plus or minus 2% of revenue, and approximately \$36.5 million, respectively.

No conference call will be held in conjunction with this guidance update. Additional information related to the third quarter will be available when the Company reports its third quarter 2011 results.

Forward-Looking Statements:

The foregoing paragraphs contain forward-looking statements that involve estimates,

assumptions, risks and uncertainties. Such forward-looking statements include statements relating to: our business outlook, including those statements relating to expected revenue, gross margin, and total operating expenses. Other forward-looking statements may be indicated by words such as “will,” “could,” “should,” “would,” “expect,” “plan,” “anticipate,” “intend,” “forecast,” “believe,” “estimate,” “predict,” “propose,” “potential,” “continue” or the negative of these terms or other comparable terminology. Lattice believes the factors identified below could cause actual results to differ materially from the forward-looking statements.

Estimates of future revenue are inherently uncertain due to, among other things, the high percentage of quarterly “turns” business. In addition, revenue is affected by such factors as global economic conditions, which may affect customer demand, pricing pressures, competitive actions, the demand for our Mature, Mainstream and New products, and in particular our MachXO and LatticeECP3 solutions, the ability to supply products to customers in a timely manner and any further unanticipated consequences associated with the Japan earthquakes and tsunami or changes in our distribution relationships. Actual gross margin percentage and operating expenses could vary from the estimates on the basis of, among other things, changes in revenue levels, changes in product pricing and mix, changes in wafer, assembly, test and other costs, variations in manufacturing yields, the failure to sustain operational improvements, the actual amount of restructuring charges and compensation charges due to stock price changes. Any unanticipated declines in revenue or gross margin, any unanticipated increases in our operating expenses or unanticipated charges, including without limitation, restructuring charges could adversely affect our profitability during the quarter.

In addition to the foregoing, other factors that may cause actual results to differ materially from the forward-looking statements in this press release include global economic uncertainty, overall semiconductor market conditions, market acceptance and demand for our new products, the Company's dependencies on its silicon wafer suppliers, the impact of competitive products and pricing, technological and product development risks, and the other risks that are described in this press release and that are otherwise described from time to time in our filings with the Securities and Exchange Commission. The Company does not intend to update or revise any forward-looking statements, whether as a result of events or circumstances after the date hereof or to reflect the occurrence of unanticipated events.

About Lattice Semiconductor:

Lattice is the source for innovative [FPGA](#), [PLD](#), programmable [Power Management](#) and [Clock Management](#) solutions. For more information, visit www.latticesemi.com. Follow Lattice via [Facebook](#), [RSS](#) and [Twitter](#).

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