

**UNITED STATES  
SECURITIES AND EXCHANGE COMMISSION**  
Washington, DC 20549

**FORM 8-K**

**CURRENT REPORT  
Pursuant to Section 13 or 15(d)  
of The Securities Exchange Act of 1934**

**Date of Report (Date of earliest event reported)**  
**September 11, 2023**

**Lattice Semiconductor Corporation**

(Exact name of registrant as specified in its charter)

**Delaware**  
(State or other jurisdiction  
of incorporation)

**000-18032**  
(Commission  
File Number)

**93-0835214**  
(IRS Employer  
Identification No.)

**5555 NE Moore Court**  
**Hillsboro, Oregon 97124**  
(Address of principal executive offices, including zip code)

**(503) 268-8000**  
(Registrant's telephone number, including area code)

(Former name or former address, if changed since last report)

[ Check the appropriate box below if the Form 8-K filing is intended to simultaneously satisfy the filing obligation of the registrant under any of the following provisions (see General Instruction A.2. below):

- Written communications pursuant to Rule 425 under the Securities Act (17 CFR 230.425)
- Soliciting material pursuant to Rule 14a-12 under the Exchange Act (17 CFR 240.14a-12)
- Pre-commencement communications pursuant to Rule 14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b))
- Pre-commencement communications pursuant to Rule 13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c))

Securities registered pursuant to Section 12(b) of the Act:

<b>Title of each class</b>	<b>Trading Symbol</b>	<b>Name of each exchange on which registered</b>
Common Stock, \$.01 par value	LSCC	NASDAQ Global Select Market

Indicate by check mark whether the registrant is an emerging growth company as defined in Rule 405 of the Securities Act of 1933 (§230.405 of this chapter) or Rule 12b-2 of the Securities Exchange Act of 1934 (§240.12b-2 of this chapter).

Emerging growth company

If an emerging growth company, indicate by check mark if the registrant has elected not to use the extended transition period for complying with any new or revised financial accounting standards provided pursuant to Section 13(a) of the Exchange Act.

**Item 5.02. Departure of Directors or Certain Officers; Election of Directors; Appointment of Certain Officers; Compensatory Arrangements of Certain Officers.**

(b)

On September 11, 2023, Lattice Semiconductor Corporation (the “Company”) announced the appointment of Pravin Desale to the position of Senior Vice President, Research and Development effective September 11, 2023.

The Company also appointed Stephen Douglass, one of the Company’s named executive officers, who has been serving in the role of Senior Vice President, Research and Development, as Senior Vice President, Chief Technology Officer effective September 11, 2023.

**Item 7.01. Regulation FD Disclosure.**

On September 11, 2023, the Company issued a press release related to Mr. Desale’s appointment as Senior Vice President, Research and Development and Mr. Douglass’ appointment as Senior Vice President, Chief Technology Officer. The press release, furnished as Exhibit 99.1 to this Current Report on Form 8-K, shall not be deemed “filed” for purposes of Section 18 of the Securities Exchange Act of 1934, as amended, nor shall it be deemed incorporated by reference in any filing under the Securities Act of 1933, as amended, except as shall be expressly set forth by specific reference in such filing.

**Item 9.01. Financial Statements and Exhibits.**

(d) Exhibits

The following exhibits are being furnished herewith:

<u>Exhibit No.</u>	<u>Description</u>
99.1	<a href="#">Press Release, dated September 11, 2023.</a>
104	Cover Page Interactive Data File (formatted as Inline XBRL).

**SIGNATURES**

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

**LATTICE SEMICONDUCTOR CORPORATION**

By: /s/ Tracy Feanny

Tracy Feanny

Senior Vice President, General Counsel and Secretary

Date: September 11, 2023

**MEDIA CONTACT:**

Sophia Hong  
Lattice Semiconductor  
503-268-8786  
[Sophia.Hong@latticesemi.com](mailto:Sophia.Hong@latticesemi.com)

**INVESTOR CONTACT:**

Rick Muscha  
Lattice Semiconductor  
408-826-6000  
[Rick.Muscha@latticesemi.com](mailto:Rick.Muscha@latticesemi.com)

**Lattice Semiconductor Appoints Pravin Desale as Senior Vice President of R&D**

**HILLSBORO, OR – Sept. 11, 2023** – [Lattice Semiconductor Corporation](#) (NASDAQ: LSCC), the low power programmable leader, today announced the appointment of Pravin Desale as Senior Vice President, Research & Development (R&D), effective immediately. Steve Douglass, who served in this role since 2018, is transitioning into a new role as Chief Technology Officer, responsible for the long-term Lattice technology strategy.

“We are happy to welcome Pravin to the Lattice leadership team as we continue executing our strategic plan. His extensive technical experience will help further strengthen our efforts to deliver breakthrough innovation to our customers,” said Jim Anderson, President and Chief Executive Officer, Lattice Semiconductor. “Additionally, on behalf of the entire Lattice team, I would like to thank Steve for his many contributions to Lattice to-date, including the rapid expansion of the company’s strongest product portfolio in our history. I look forward to the impact he will drive in his new role.”

“I’m excited to join Lattice and look forward to working with the talented engineering team to continue building on the strong silicon, software, and solution portfolio the company has established over the past years,” said Pravin Desale, Senior Vice President, R&D, Lattice Semiconductor.

Mr. Desale has more than 30 years of R&D and technology leadership experience delivering complex silicon, software, and application-centric solutions for a variety of market verticals. He most recently served as Senior Vice President of IoT Engineering at Semtech. Prior to the Semtech acquisition of Sierra Wireless, Pravin was the Senior Vice President of Engineering at Sierra Wireless where he was responsible for all hardware and software development. He also held previous technical leadership roles at Veritas Technologies, Seagate, and LSI Corporation. He has a Master of Electrical Engineering degree from Santa Clara University and a Bachelor of Electronics and Telecommunications degree from the College of Engineering Pune Technological University.

**About Lattice Semiconductor**

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing Communications, Computing, Industrial, Automotive, and Consumer markets. Our technology, long-standing relationships, and commitment to world-class support let our customers quickly and easily unleash their innovation to create a smart, secure, and connected world.

For more information about Lattice, please visit [www.latticesemi.com](http://www.latticesemi.com). You can also follow us via [LinkedIn](#), [Twitter](#), [Facebook](#), [YouTube](#), [WeChat](#), or [Weibo](#).

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## Forward-Looking Statements

Statements in this press release that refer to future plans and expectations are forward-looking statements that involve a number of risks and uncertainties. Words such as “anticipates,” “expects,” “intends,” “goals,” “plans,” “believes,” “seeks,” “estimates,” “continues,” “may,” “will,” “would,” “should,” “could,” and variations of such words and similar expressions are intended to identify such forward-looking statements. Such forward-looking statements include, but are not limited to, statements relating to executing our strategic plan, delivering breakthrough innovation, the creation and rapid product portfolio expansion, and expansion of our low power programmable leadership. A number of important factors could cause actual results to differ materially from our expectations and actual results are subject to risks and uncertainties that relate more broadly to our overall business, including those described in Item 1A in Lattice’s most recent Annual Report on Form 10-K and as may be supplemented from time-to-time in Lattice’s other filings with the Securities and Exchange Commission, all of which are expressly incorporated herein by reference.

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Lattice Semiconductor Corporation, Lattice Semiconductor (& design), and specific product designations are either registered trademarks or trademarks of Lattice Semiconductor Corporation or its subsidiaries in the United States and/or other countries. The use of the word “partner” does not imply a legal partnership between Lattice and any other entity.

**GENERAL NOTICE:** Other product names used in this publication are for identification purposes only and may be trademarks of their respective holders.