

**UNITED STATES  
SECURITIES AND EXCHANGE COMMISSION**

Washington, DC 20549

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**FORM 8-K**

**CURRENT REPORT  
Pursuant to Section 13 or 15(d) of  
The Securities Exchange Act of 1934**

Date of Report (Date of earliest event reported)  
**June 14, 2007**

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**Lattice Semiconductor Corporation**

(Exact name of registrant as specified in its charter)

**Delaware**

(State or other jurisdiction of incorporation)

**000-18032**

(Commission File Number)

**93-0835214**

(IRS Employer  
Identification No.)

**5555 N. E. Moore Court**

**Hillsboro, Oregon 97124-6421**

(Address of principal executive offices, including zip code)

**(503) 268-8000**

(Registrant's telephone number, including area code)

(Former name or former address, if changed since last report)

Check the appropriate box below if the Form 8-K filing is intended to simultaneously satisfy the filing obligation of the registrant under any of the following provisions (see General Instruction A.2. below):

- Written communications pursuant to Rule 425 under the Securities Act (17 CFR 230.425)
  - Soliciting material pursuant to Rule 14a-12 under the Exchange Act (17 CFR 240.14a-12)
  - Pre-commencement communications pursuant to Rule 14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b))
  - Pre-commencement communications pursuant to Rule 13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c))
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**Item 7.01. Regulation FD Disclosure.**

On June 14, 2007, Lattice Semiconductor Corporation posted on its website a business update for the second quarter of the fiscal year 2007. A copy of the business update is furnished as Exhibit 99.1 to this report.

The information in this Item 7.01 and in Exhibit 99.1 shall not be deemed to be “filed” for the purposes of Section 18 of the Securities Exchange Act of 1934, as amended (the “Exchange Act”), or incorporated by reference in any filing under the Securities Act of 1933, as amended, or the Exchange Act, except as shall be expressly set forth by specific reference in such filing.

**Item 9.01. Financial Statements and Exhibits.**

(d) Exhibits.

<u>Exhibit No.</u>	<u>Description</u>
99.1	Business Update dated June 14, 2007 (furnished, not filed, herewith)

**SIGNATURES**

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

**LATTICE SEMICONDUCTOR CORPORATION**

Date: June 14, 2007

By: /s/ Martin R. Baker

Martin R. Baker  
Corporate Vice President, General Counsel and  
Secretary

**EXHIBIT INDEX**

<b>Exhibit No.</b>	<b>Description</b>
99.1	Business Update dated June 14, 2007 (furnished, not filed, herewith)

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**For more information contact:**  
**Jan Johannessen**  
Chief Financial Officer  
Lattice Semiconductor Corporation  
503-268-8000

## **LATTICE SEMICONDUCTOR UPDATES GUIDANCE FOR SECOND QUARTER**

**HILLSBORO, OR – June 14, 2007** - Lattice Semiconductor (NASDAQ: LSCC) today announced its business update for the second quarter of 2007.

The Company reaffirms that it expects second quarter revenue to be 0% to 4% higher than the first quarter of 2007.

The Company's New Products are showing a strong increase in growth in the second quarter as compared to the prior quarter.

No conference call will be held in conjunction with this guidance update.

### **Forward-Looking Statements**

The foregoing business update contains forward-looking statements. Lattice believes the factors identified below in connection with each such statement could cause actual results to differ materially from the forward-looking statements.

Estimates of future revenue are inherently uncertain due to the high percentage of quarterly "turns" business, as well as such factors as pricing pressures, competitive actions, the demand for our products, and our ability to supply products to customers in a timely manner. Actual gross margin percentage and operating expenses could vary from the estimates contained herein on the basis of, among other things, changes in revenue levels, changes in product pricing and mix, changes in wafer, assembly and test costs, variations in manufacturing yields, and changes in stock-based compensation charges due to stock price changes.

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In addition to the foregoing, other factors that may cause actual results to differ materially from the forward-looking statements herein include the Company's dependencies on its silicon wafer suppliers, technological and product development risks, and other risks that are described from time to time in our filings with the Securities and Exchange Commission. The Company does not intend to update or revise any forward-looking statements, whether as a result of events or circumstances after the date hereof or to reflect the occurrence of unanticipated events.

## **About Lattice Semiconductor**

Lattice Semiconductor Corporation provides the industry's broadest range of Programmable Logic Devices (PLD), including Field Programmable Gate Arrays (FPGA), Complex Programmable Logic Devices (CPLD), Mixed-Signal Power Management and Clock Generation Devices, and industry-leading SERDES products.

Lattice continues to deliver "More of the Best" to its customers with comprehensive solutions for system design, including an unequalled portfolio of high-performance, non-volatile and low-cost FPGAs.

Lattice products are sold worldwide through an extensive network of independent sales representatives and distributors, primarily to OEM customers in communications, computing, industrial, consumer, automotive, medical and military end markets. For more information, visit <http://www.latticesemi.com>.

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