Corporate Overview

November 2026

Safe Harbor Statement

Forward-Looking Statements: This presentation contains forward-looking statements within the meaning of the Private Securities Litigation Reform Act of 1995 and the safe harbor provisions of the Securities Act of 1933 and Securities Exchange Act of 1934. Forward-looking statements include statements regarding our expectations, beliefs, plans, objectives, assumptions and future events or performance, including: revenue, EPS growth, free cash flow, operating leverage, margin expansion and other financial performance; business outlook and guidance; market recovery and growth, including AI- and data center-related growth; design wins, bookings and backlog; market share, new-product revenue and customer adoption; the expected benefits, performance, availability and adoption of our products, solutions, collaborations and partnerships; firmware, boot management and infrastructure manageability solutions; physical AI applications; post-quantum cryptography; AMI integration, synergies, revenue contribution and other expected benefits; attach rates and ASPs; debt reduction; share repurchases; and the Business Outlook. Forward-looking statements may be identified by words such as “will,” “could,” “may,” “expect,” “plan,” “project,” “anticipate,” “forecast,” “believe,” “estimate,” “outlook,” “potential,” “continue” and similar expressions.

Factors that could cause actual results to differ materially include: global economic conditions and semiconductor industry cyclicality; customer and distributor purchasing patterns, inventory and order timing; pricing and inflation; competition, including customers’ internal development efforts and open-source or alternative firmware and software solutions; trade disputes, sanctions, tariffs, export controls and license requirements; risks associated with integrating AMI and realizing anticipated benefits, synergies, cost savings and accretion; risks relating to our indebtedness, including debt service, covenant restrictions and variable interest rates; potential impairment of goodwill or acquired intangible assets; concentration of demand among customers, distributors and end markets, including AI and data-center infrastructure, and the risk that anticipated demand is not sustained; dependence on third-party foundry, assembly and test providers and sufficient manufacturing capacity and supply; retention of key employees; cybersecurity and product and firmware security risks, including vulnerabilities in third-party or open-source components; changes in product, license, service and segment mix; manufacturing yields and costs; technological change and our ability to timely develop and introduce products; difficulties developing, releasing, updating or supporting firmware and software; intellectual property and litigation risks; and other risks described in our SEC filings, including our most recent Forms 10-K and 10-Q, particularly under “Risk Factors” and “Management’s Discussion and Analysis of Financial Condition and Results of Operations.”

Forward-looking statements regarding the completed AMI acquisition include the expected benefits of the transaction; expansion of our addressable market and future product and solution opportunities; integration of AMI; continued development and delivery of silicon- and ecosystem-neutral solutions; customer, partner and industry relationships; anticipated accretion to margins, free cash flow and earnings; achievement of a $1 billion or greater annual revenue run rate; and future revenue growth. References to AMI-related accretion, profitability, margins, free cash flow, earnings, revenue run rates or revenue growth are based on management’s current expectations and assumptions, apply on a non-GAAP basis unless otherwise stated, and are subject to risks including integration, retention of key personnel, realization of anticipated synergies and the possibility that the transaction does not deliver anticipated results.

Actual results could differ materially from forward-looking statements. Forward-looking statements speak only as of the date made, and we undertake no obligation to update or revise them.

Use of Non-GAAP Financial Information: This presentation includes non-GAAP financial measures, including non-GAAP gross margin, operating expenses and EPS, adjusted EBITDA and free cash flow. These measures are not substitutes for GAAP and should be considered together with our GAAP results. See our latest earnings materials for reconciliations to the most directly comparable GAAP measures. For forward-looking non-GAAP measures, we cannot provide reconciliations without unreasonable efforts because certain necessary items are unavailable on a forward-looking basis and cannot be reasonably predicted due to uncertainty regarding their amounts and timing.

Trademarks – General Notice
Lattice Semiconductor Corporation, Lattice Semiconductor (& design) and specific product designations are registered trademarks or trademarks of Lattice Semiconductor Corporation or its subsidiaries in the United States and/or other countries.

Table of Contents

- Company Overview
- Products & Solutions
- Recent Announcements
- End Markets & Applications
- Financials

Company Overview



Our Mission

LATTICE SEMICONDUCTOR

The Low Power Programmable & Platform Firmware Leader

DIFFERENTIATED
Small and Mid-range FPGA and Firmware Solution Portfolio

DIVERSIFIED
Across Largest and Fastest Growing Applications

DELIVERING
Faster Than Industry Growth

Lattice Semiconductor Overview

APPLICATIONS & MARKETS

COMPUTE & COMMUNICATIONS

59%

INDUSTRIAL & EMBEDDED

28%

AMI

13%

WORLD CLASS SUPPLIER

#1
World's largest volume supplier of FPGA

Tier 1
Supplier with 40+ years of innovation

20+
Year average product longevity

GROWING CUSTOMER BASE

ABB, Accton, amazon, BOSCH, BYD, Canon, cisco, DELL, DESAY SV, ERICSSON, Ford, FUJITSU, Google, hp, Hewlett Packard Enterprise, HITACHI, INOVANCE, Juniper Networks, Lenovo, Lockheed Martin, Meta, Microsoft, NOKIA, Panasonic, Raytheon, SAMSUNG, Schneider Electric, SIEMENS, SONY

GLOBAL SUPPORT



Applications & Markets percentages based on Q3'26 revenue breakdown by reporting segment.

Holding Ourselves to the Highest Corporate Stewardship Standards

CULTURE OF INNOVATION



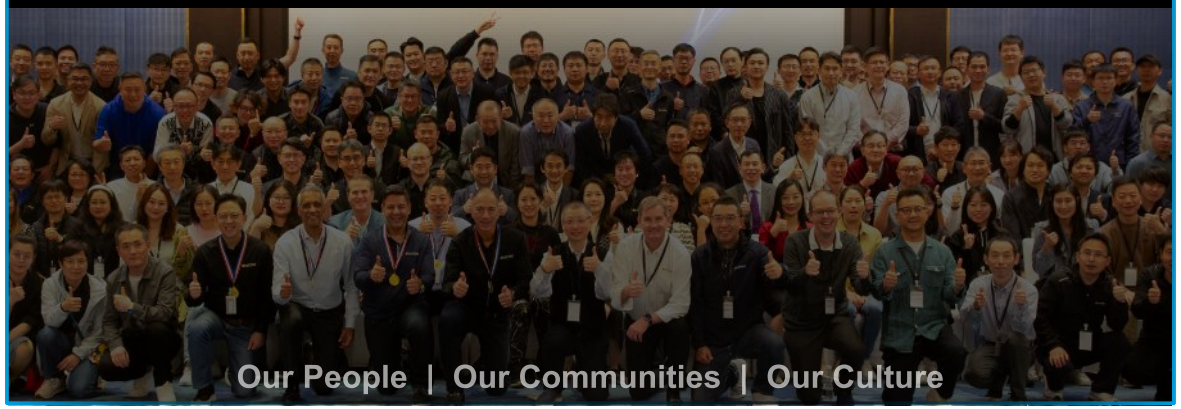
The Low Power Programmable Leader

ENVIRONMENTALLY CONSCIOUS



Operational Excellence | Supply Chain Management

INCLUSION & SOCIAL WELLBEING



Our People | Our Communities | Our Culture

TRANSPARENCY & INTEGRITY



Governance Principles | Ethical Standards | Continuous Improvement

GSA MOST RESPECTED PUBLIC COMPANY FIVE YEARS IN A ROW

STRONG & GROWING RECOGNITION FOR CLEANTECH PRODUCT INNOVATION

More information available here

Lattice Executive Leadership Team



Ford Tamer
CEO



Pravin Desale
R&D



Esam Elashmawi
CSMO



Tracy Feanny
General Counsel



Lorenzo Flores
CFO



Sanjoy Maity
AMI Business Unit



Divyesh Shah
Operations & Quality



Erhaan Shaikh
Sales



Nicole Singer
Human Resources

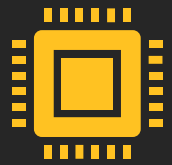
Products & Solutions



Lattice Value Proposition

LATTICE SEMICONDUCTOR

LOW POWER PROGRAMMABLE FPGA LEADERSHIP



Lowest Power | Smallest Size | Ease of Use

BOOT FIRMWARE & INFRASTRUCTURE MANAGEABILITY LEADERSHIP



Secure | Stable | Scalable

The Industry's Most Complete Secure Management and Control Platform

Bringing Silicon Control and Firmware Management Together

LATTICE SEMICONDUCTOR

FPGA



Low Power,
Deterministic,
Programmable



ami
A LATTICE COMPANY

PLATFORM FIRMWARE

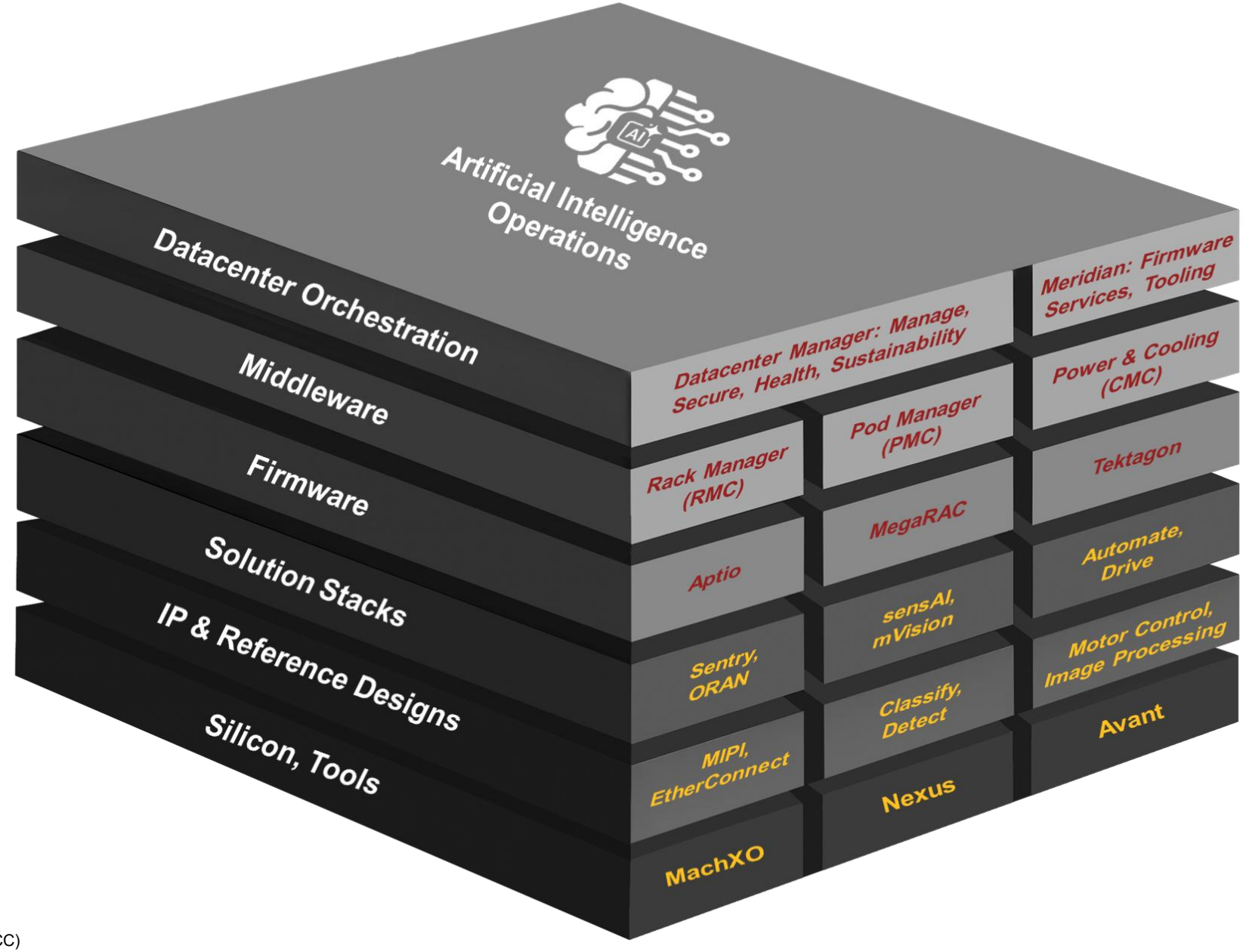


Boot,
Manageability,
Security

Open Secure Management and Control Plane
From Silicon to Rack to Grid

LATTICE SEMICONDUCTOR

The Industry's Most Complete Secure Management & Control Platform



Recent Announcements



Launched 4 New Products, 4 New Solutions



PQC Ready, Physical Secure Control Small FPGA

LATTICE PROMPT™

Unlocking Significant FPGA Developer Productivity

LATTICE Sentry™

VERSION 5.0

Industry-First PQC-Enabled PFR and Attestation for AI Infrastructure

Rm AMI Rack Manager™

Bringing Intelligent Rack-Level Management to AI Infrastructure

LATTICE SEMICONDUCTOR + ami™ A LATTICE COMPANY



Delivering Joint Secure Control and Management Solutions

Introduced SCMP (now SCaMPi) at 2026 OCP Global Summit

NEW OCP WORKSTREAM

Secure Control and Management Plane

Discover | Verify | Coordinate | Recover | Validate

FOUNDING CONTRIBUTORS

ami (A LATTICE COMPANY) · LATTICE SEMICONDUCTOR · AMD · arm · ASPEED · intel · JABIL · Microsoft · NXP

ECOSYSTEM PARTICIPANTS

Operators | ODMs / OEMs | Silicon Providers | Firmware Providers | Power & Cooling | Open-source Communities

INDUSTRY FOUNDATIONS

OPEN Compute Project · OpenBMC · Caliptra · tianocore · DMTF

Low Power Programmable FPGA Leadership



Lattice FPGAs Are Everywhere – Primary and Companion Chips

PRIMARY FUNCTIONS

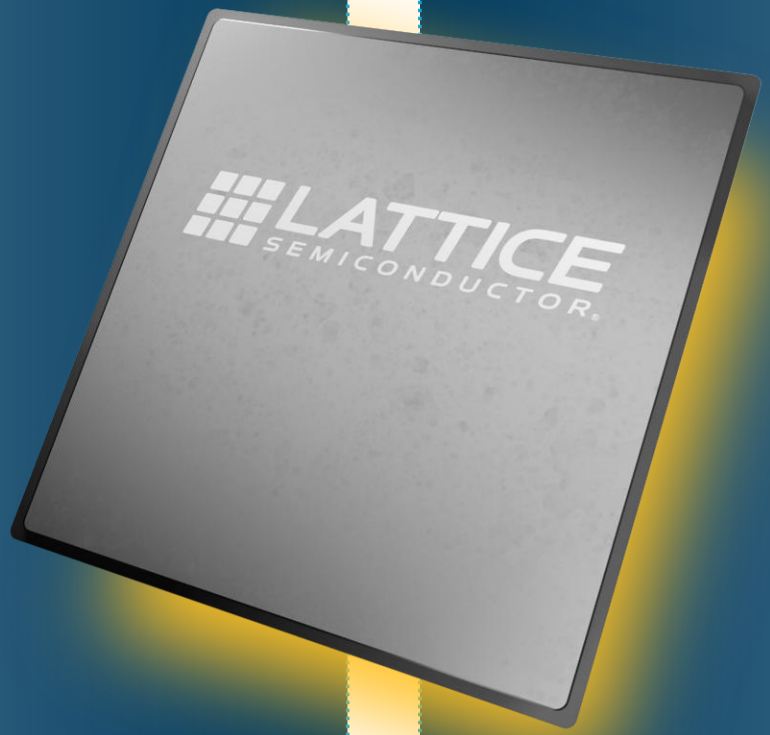
Motion Control

Signal Processor

Image Processing

Real Time Networking

Far Edge AI



COMPANION FUNCTIONS

Bridging

I/O Expansion

Sensor Fusion

Aggregation

Root of Trust

Board Management

Supporting Top AI Accelerator, XPU, Switch, NIC, Sensors

FPGA Platform Leadership



Architected for applications requiring up to 16G SERDES and up to 200k LCs



Architected for applications requiring up to 25G SERDES and up to 500k LCs

LOWER POWER

FASTER PERFORMANCE

SMALLER SIZE

Lattice FPGA Portfolio

PLATFORM	DEVICE FAMILIES								
LATTICE AVANT	Avant™-E	Avant™-G	Avant™-X	Avant™ LP					
									
	Edge-optimized Processing	Cutting-edge General Purpose Processing	Advanced Connectivity	Low Power, High Performance Optimized					
LATTICE NEXUS 2	Certus™-N2	Mach™-N2							
									
	High Performance General Purpose Processing	Enhanced System Monitor and Control							
LATTICE NEXUS	CrossLink™-NX	Certus™-NX	Mach™-NX	CertusPro™-NX	MachXO5™-NX	MachXO5T™-NX	CrossLinkU™-NX	MachXO5™-NX TDQ	
									
	Embedded Vision Processing	General Purpose Processing	Next Gen Hardware Security	Advanced General Purpose Processing	Enhanced System Monitor and Control	Advanced System Control	Embedded Vision Processing with USB	Industry 1st PQC-Enabled FPGA	

Software Solution Stack Portfolio

Lattice sensAI™



Low Power Edge AI

High Performance Inferencing Under 1W

Supports Industry Standard ML Frameworks

Complete Solution Enablement

Lattice mVision™



Low Power Embedded Vision

Flexible Image Sensor Bridging & Aggregation

Image Processing Integration

Complete Solution Enablement

Lattice Sentry™



Cyber Resilient Root of Trust

Secure Hardware Creates Root-of-Trust for Systems

Cryptographically Secured Supply Chain

Protection Against Cloning, Counterfeiting, Trojan Insertion, & Simulation

Lattice Automate™



Accelerating Factory Automation

Accelerates Industrial Automation Development

Supports Use Cases Like Motor Control, Real-time Networking, & Predictive Maintenance

Complete Solution Enablement

Lattice ORAN™



Enabling ORAN Deployment

Enables Zero Trust Security and Data Protection in Networks

Flexible, Tight Fronthaul Synchronization

Acceleration with Low Power

Lattice Drive



Adaptable Automotive Design

DisplayPort Connectivity

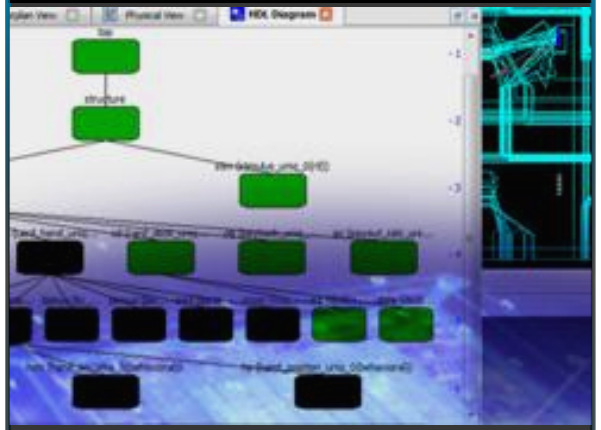
Video Scaling Up to 4K

Local Dimming for Contrast Enhancement

Bridging & Networking

Easy-to-use Software

LATTICE DIAMOND®



Powerful FPGA Design & Verification Environment

Easy Design Exploration

Easy to Use Powerful Tools

Optimized for Lattice Devices

LATTICE RADIANT®



Best-in-class, Easy-to-use FPGA Design Software

Simplified Flow for Faster Design

Increase Re-use with IP Tools

Leading Synthesis & Simulation

LATTICE PROPEL™



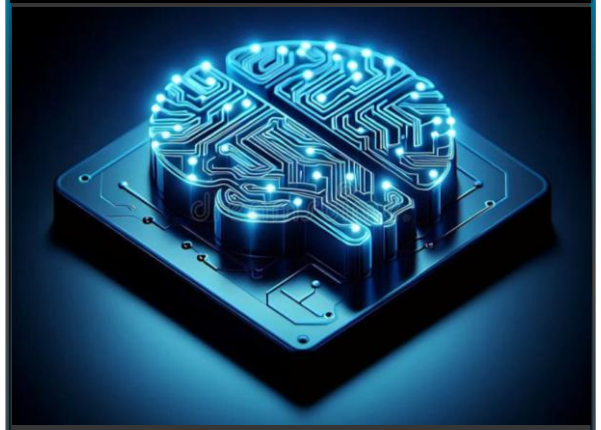
Complete Toolset for Embedded System Design

IP System Integration Environment

Software Development Kit & Libraries

Build, Compile, Analyze, Debug

LATTICE PROMPT™



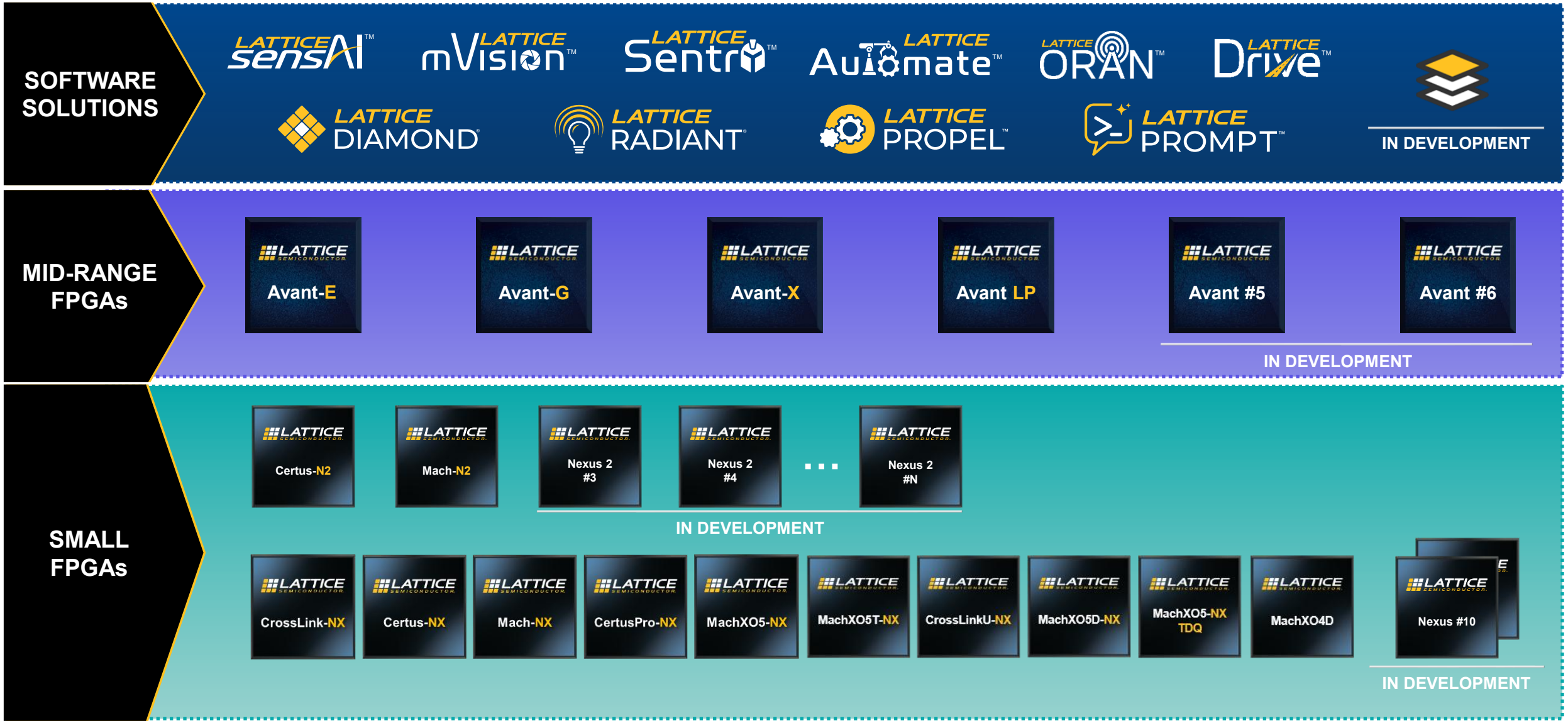
AI-Driven FPGA Design Tool

Design in Natural Language with Preferred AI Tools

Integrated Lattice Expertise

Achieve Productivity Gains of 10X and More

Continued Investment in Low Power Programmable Leadership



Note: Subject to change.

Boot Firmware & Infrastructure Manageability Leadership



AMI Leads in BIOS and Manageability for Cloud and AI

ami™
A LATTICE COMPANY

Boot Firmware

Ap AMI Aptio™

Core boot firmware franchise with AI scale, stability, and strong hyperscaler and OEM/ODM adoption

- Installed base driving AI expansion
- Stable revenue engine
- Platform expansion foundation

Manageability & Security

Mg AMI MegaRAC™

Dm AMI Data Center Manager™

Te AMI Tektagon™

Strategic growth engine for manageability, security, and AI infrastructure

- Expands platform control
- Supports AI and rack growth
- Builds recurring value

LATTICE SEMICONDUCTOR

AMI Powers Modern Compute Infrastructure

ami™
A LATTICE COMPANY

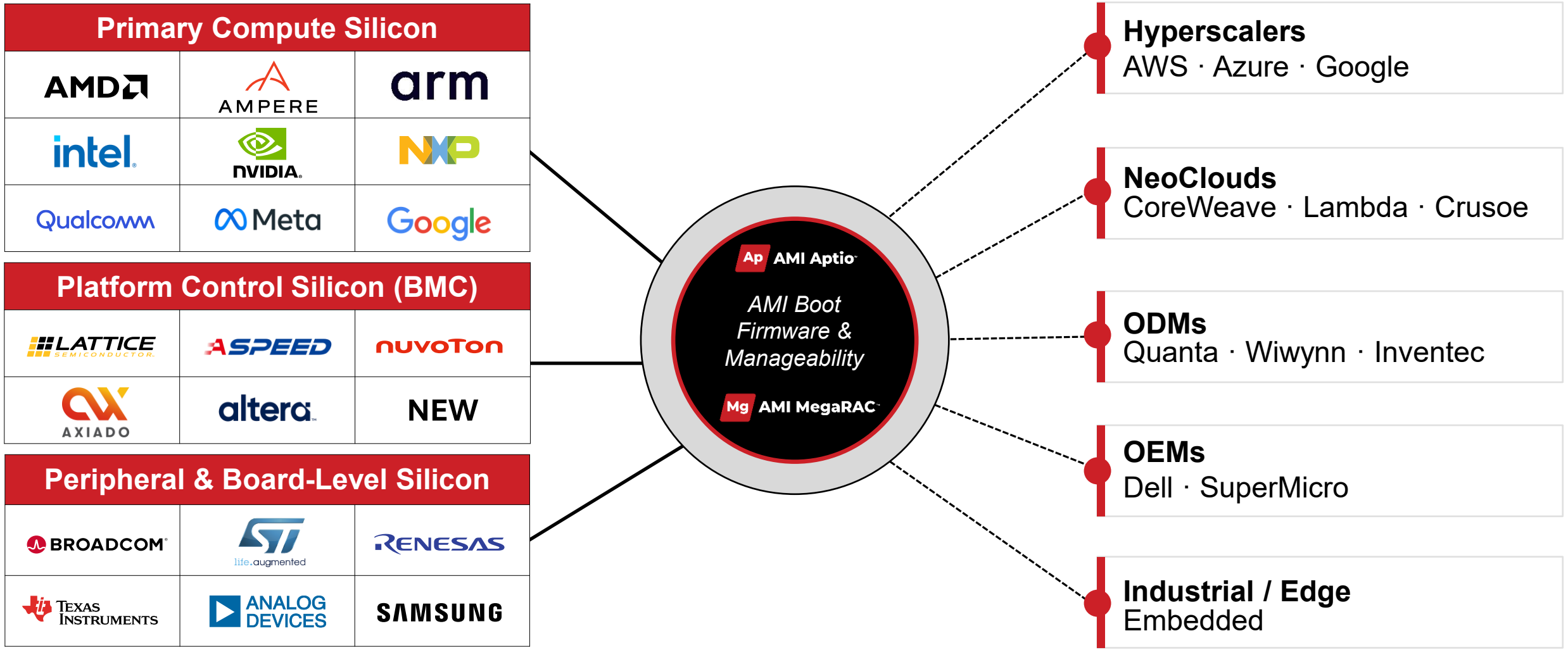


Dm AMI Data Center Manager™	**OPERATE** Operates rack, fleet, and data center infrastructure
Te AMI Tektagon™	**SECURE** Establishes trust and recover with resilience
Mg AMI MegaRAC™	**MANAGE** Monitors, controls and updates systems
Ap AMI Aptio™	**BOOT** Initializes and enables compute platforms

Trusted platform firmware, infrastructure management, and security that accelerates customer platform deployment and scalability

LATTICE SEMICONDUCTOR

AMI Connects and Manages All Silicon, Platforms, and Customers



AMI sits at the critical intersection of silicon, firmware, and software — enabling servers, AI clusters, cloud infrastructure, and edge systems to boot, run, and be managed reliably

End Markets & Applications



Innovation Leadership to Amplify Customer Success

COMPUTE



aws · DELL Technologies · Google · H3C · Hewlett Packard Enterprise · Lenovo · Meta · Microsoft · NetApp · SUPERMICRO

Leading Server & Cloud Providers

COMMUNICATIONS



Adtran · ARISTA · ciena · cisco · ERICSSON · FUJITSU · JUNIPER NETWORKS · NEC · NOKIA · SAMSUNG

Leading Comms OEMs

INDUSTRIAL



ABB · EMERSON · GE · Honeywell · OMRON · Panasonic · Rockwell Automation · Schneider Electric · SIEMENS

Leading Industrial OEMs

EMBEDDED



AIRBUS · BECKHOFF · BYD · Canon · GENERAL DYNAMICS · GE Healthcare · HITACHI · TESLA · THALES · TOYOTA

Leading Embedded System Providers

Manage Complexity, Increase Uptime, Accelerate Time to Market

Automotive deployments in select models via direct or tier one engagements

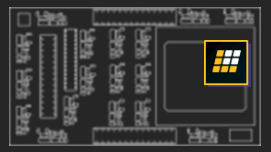
Lattice, AMI Committed to Companion Chip and Solution Neutrality



GPU/TPU AI Accel.	CPU	Switch, NIC	MPU/MCU	BMC	Sensor	Analog
NVIDIA	intel	NVIDIA	NXP	ASPEED	SONY	ANALOG DEVICES
AMD	AMD	BROADCOM	ST	nuvoTon	OMNIVISION	Texas Instruments
Google	arm	MARVELL	MEDIATEK	NXP	HESAI	Infineon
amazon	NVIDIA	AMD	Qualcomm	HPE	SICK	ROHM SEMICONDUCTOR
Microsoft		REALTEK	RENESAS	DELL	SAMSUNG	
Meta		cisco		AXIADO	Valeo	
Alibaba Cloud				StarFive	TELEDYNE FLIR	
ByteDance					MAGNA	
					e-con Systems	

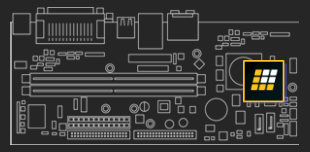
Lattice Solves Data Center Challenges

CARDS & MODULES



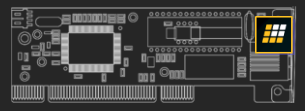
Hardware Acceleration

Offload · Attestation



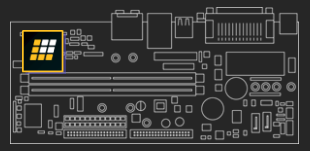
Storage Card

Key Mgmt · Hot Swap



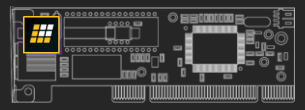
GPU Card

Power · Throttling



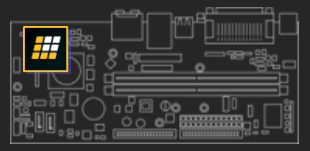
Network Card

Power Control · Offload



Add-in Cards

Control · Bridging



Secure Control Module

BMC · PFR · Attestation

SERVER



- USB 2.0 / LTPI · Robust IP Connectivity
- Bridging · I/O · Board, Fan & Power Control
- Hardware Abstraction · BMC / SMC Companion
- HRoT · DICE · Trusted Firmware · Attestation

RACK



- Rack-Level Trust & Attestation
- Low-Latency Deterministic Leak Response
- Rack Telemetry · RAS · Manageability
- Power Control · Throttling · CDU / PDU

TRUST & CONTROL

SPDM Attestation | Silicon Provenance | CNSA 2.0 PQC & Crypto Agility | Platform Control

MANAGEABILITY

Modular Plug & Play | Fault Response

LOW POWER, SECURE CONTROL AND MANAGEMENT PLANE

Lattice Solves Communications Challenges



Scalable Hardware Management



Control Functions



5G ORAN

Datapath



Manageability



Security

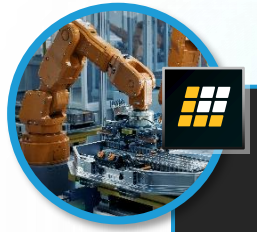


ASIC Bridging



PCIe Bridging

Lattice Solves Industrial Challenges



Smart Factory

Collision Avoidance
Edge Computing
Functional Safety
Industrial Networking
Machine Vision
Motor Control
Predictive Maintenance
Programmable Logic Control
Object Identification
CRA Manageability
Boot Firmware



Test & Measurement

High-Speed Data Acquisition
Signal Processing
Emulation and Validation
Pattern Generation and Analysis
Timing Analysis
Error Detection and Correction
Jitter and Noise Measurement
Power Analysis
Temperature and Stress Testing
Portables and Handhelds



Medical

Digital Endoscopy Systems
MRI and CT Image Processing
Ultrasound Signal Processing
Electrocardiogram Signal Processing
X-ray Processing
Blood Analysis Equipment
Health Monitors
Robotic Surgery Assistants
Secure Medical Data Processing
Patient Monitoring Systems



Robotics

Sensor Fusion
Articulated Robot Manipulator (ARM)
Autonomous Mobile Robot (AMR)
Cobots
Humanoids
Last Mile Delivery Drones & Pods
Safety and Security
CRA Manageability
Boot Firmware



Aerospace & Defense

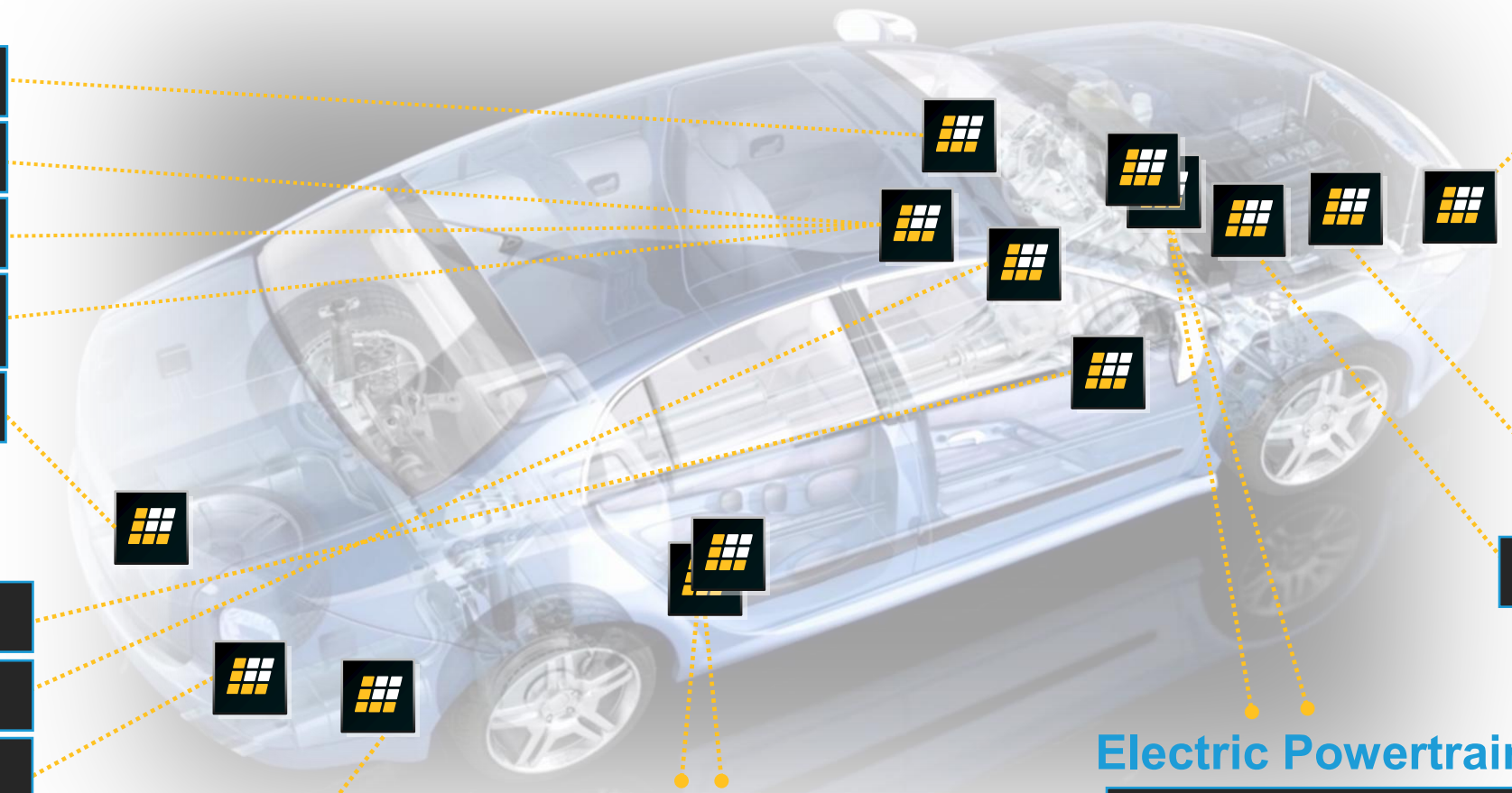
UAV and Drones
Avionics Control Systems
Radar Signal Processing
Digital Beamforming
Satellite Communications
GPS and Navigation Systems
Infrared and Optical Image Processing
Ruggedized Systems for Harsh Environments
Secure Communications
CRA Manageability
Boot Firmware



Broadcast / ProAV

Video Encoding/Decoding
Live Video Streaming
High-Resolution Video Processing
Video Scaling and De-interlacing
Color Correction and Enhancement
Image Stabilization
Audio Processing and Mixing
Multi-Protocol Bridging
Real-Time Graphics Overlays
Low-Latency Switching

Lattice Solves Automotive Challenges



Infotainment

- Display Bridging
- Local Dimming
- Display Safety
- Daylight Enhancement
- ISP

ADAS

- E-Mirror/CMS
- Thermal Camera
- Radar Sensor Bridging & Aggregation
- Lidar Sensor Bridging & Aggregation

Networking

- Zonal / Central Gateway
- Network Bridge

Electric Powertrain

- Inverter / Charger
- Battery Management

Lighting

- Smart Lamps

Security

- PQC HRoT
- Platform Security

Lattice Solves Prosumer Challenges

Drones



AR / VR / XR



Wearables





Audio Speakers

Action Cameras





Consumer Robots



Displays

Audio/Video Receivers



Lattice Drives Data Center AI Innovation

Gen AI Servers



The Trusted Control Plane for AI Servers

Secure. Manage. Accelerate.

PQC Hardware Root of Trust

Real Time Deterministic Control

Rack Manageability



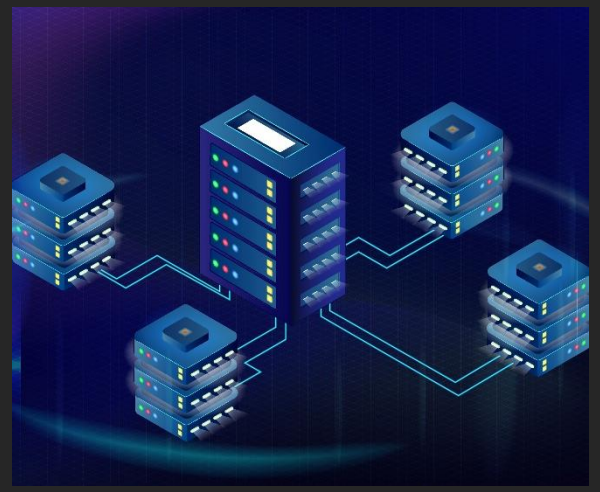
Complete Rack Visibility and Control

Monitor. Control. Orchestrate.

Simplified AI Operations

Trust at Every Layer

Infrastructure



Orchestrating AI from Server to Cluster

Scale Without Limits

Unified Infrastructure Visibility

Optimize Every Watt

Lattice Drives Physical AI Innovation

Robotics/Cobots



Sensor Bridging and Low Power Processing

Sensor Bridging and Connectivity

Real-time, Low Power Edge Processing and Motion Control

Autonomous Machines



Motor Control and Sensor Fusion

Low Latency, Deterministic Motor Control in Small Packages

Low Power Sensor Fusion and Processing/Compute Offload

Humanoids



Sensor Aggregation and Safety/Security

Multi Sensor Aggregation, Synchronization and Transport

Safe and Secure Operation

Enabling the Full Physical AI Stack – Real-time, Safe, & Power-efficient Companion Chip Solutions

Lattice Drives Security Innovation

Strong RoTs



Unique FPGA Based Hardware Roots of Trust

Integrated Lockable Dual-Boot Flash – Undeniable Service

Hardened NIST Qualified Cryptographic Algorithms

Cyber Resiliency

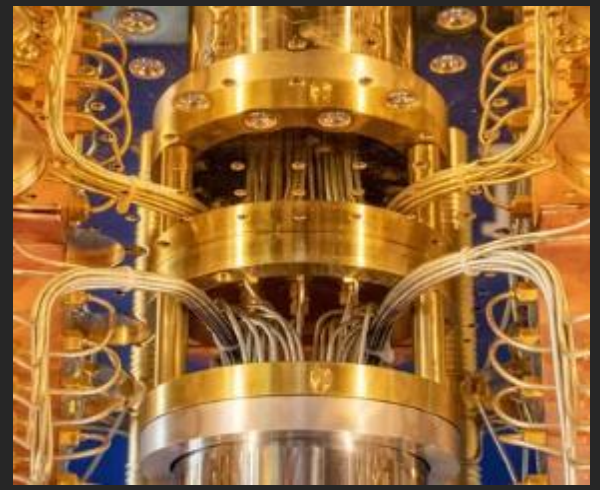


Cyber Resilient Pioneers

Processor Independent Platform Firmware Resiliency (PFR)

Cyber Resilience Act (CRA) Ready

Post Quantum



Post Quantum Crypto (PQC) Agility

PQC Ready With Latest NIST Approved PQC Algorithms

Crypto-Agility For In-field Updates & Upgrades as PQC Evolves

Enabling Next Generation Dynamic Security – Multi-channel Real-time System Protections

Financials



Lattice Q3 2026 Earnings Overview & Recent Highlights

RECORD REVENUE

$291.0M

118% Growth YoY

GROSS MARGIN

70.9%

EBIDTA

45.0%

EPS

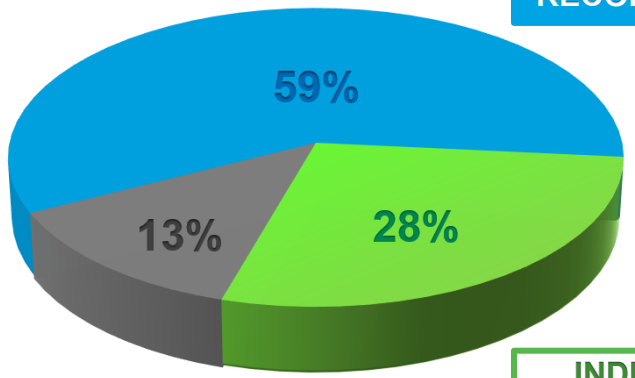
$0.72

160% Growth YoY

"We delivered record third quarter revenue of $291 million and non-GAAP EPS of $0.72, up 118% and approximately 160% year-over-year, respectively. FPGA revenue grew approximately 90%, fueled by accelerating data center AI demand and continued momentum across Industrial & Embedded applications. Strong bookings, backlog and design wins support our fourth-quarter revenue outlook of approximately $335 million and reinforce our confidence in growth into 2027 and beyond. The addition of AMI meaningfully expands our opportunity in security, control and management solutions. Early commercial momentum across security retrofit, rack boot, plug and play, and power and cooling demonstrates the value of our combined portfolio, positioning Lattice to capture greater content per platform, deepen strategic customer relationships and drive sustainable long-term growth." – Ford Tamer, CEO

End Market Overview

Q3'26 REVENUE BY SEGMENT (%)



COMPUTE & COMMUNICATIONS

+132% YoY | +36% QoQ

RECORD REVENUE

AMI

DEAL CLOSED JULY 27

INDUSTRIAL & EMBEDDED

+35% YoY | +7% QoQ

ACCELERATING

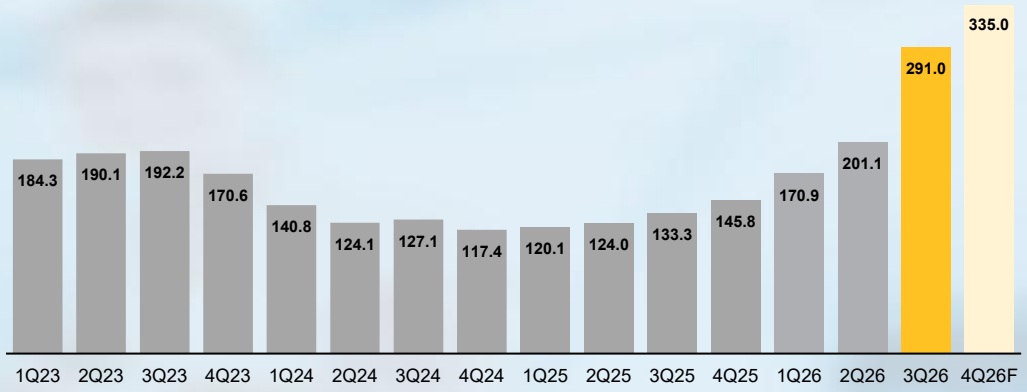
Highlights

- Achieved record new product revenue growth
- Introduced new SCMP OCP workstream alongside AMD, Arm, Aspeed, Intel, Jabil, Microsoft, and NXP
- Launched new product and solution portfolio additions:
  - 4 new solutions: Security Retrofit, Plug-and-Play, Rack Boot, and Power and Cooling
  - Lattice Prompt AI FPGA design tool
  - Lattice Mach-N2 secure control FPGA family
  - Lattice Sentry solution stack v5.0 with AMI Tektagon
  - AMI Rack Manager 1.0 for rack-scale AI infrastructure
  - AMI Data Center Manager 6.2
- Announced new Lattice and AMI companion solutions:
  - AI data center collaboration with Arm for AGI CPU server platforms
  - AMI Aptio V and AMI MegaRAC OneTree firmware support for 6th Gen AMD EPYC™ processors
  - Production-ready support for the SK Hynix RAS Library in AMI MegaRAC OneTree

Non-GAAP based on earnings reported Nov. 3, 2026. Non-GAAP measures are for informational purposes and should be considered together with GAAP. See latest earnings materials for reconciliations. Forward-looking statements apply. See 'Safe Harbor Statement'.

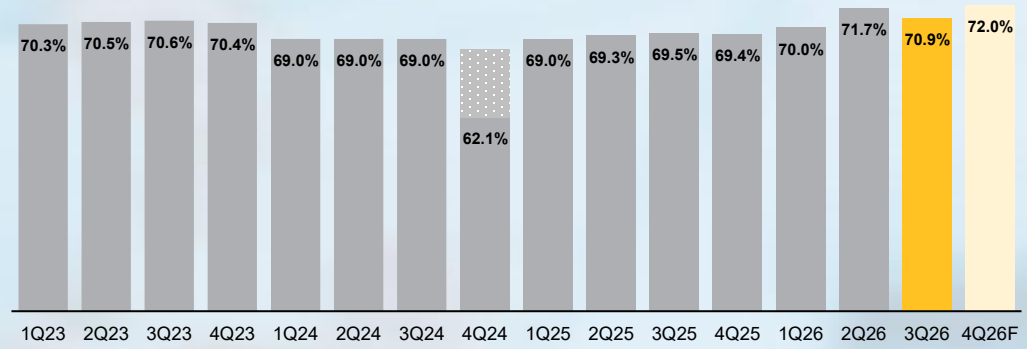
Q3 2026 Financial Results

REVENUE ($M)



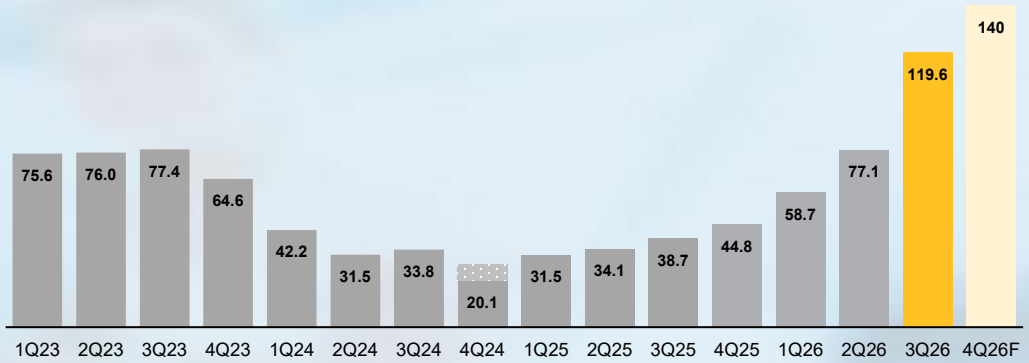
118% Revenue Growth YoY

GROSS MARGIN (%)



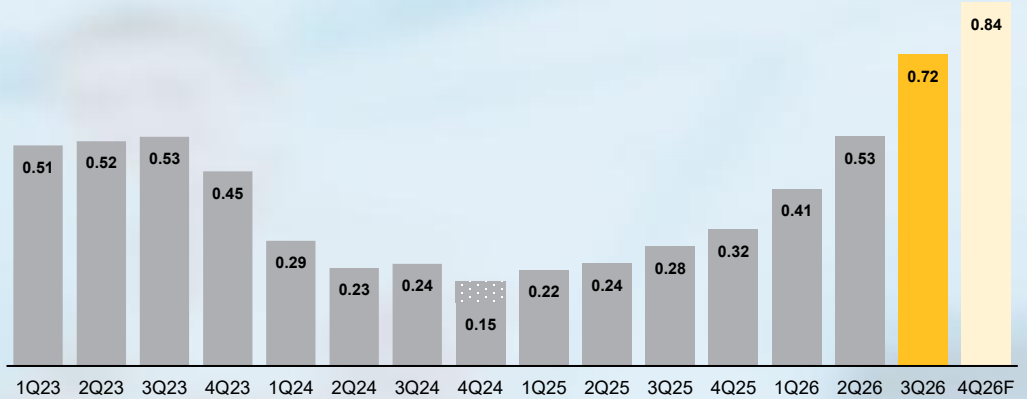
Gross Margin at 70.9%

OPERATING PROFIT ($M)



209% Growth YoY

EARNINGS PER SHARE ($)



157% Growth YoY

Non-GAAP based on earnings reported Nov. 3, 2026; Q4'24 shown with and without a one-time $7M COGS expense. 4Q26F represents forecast and business outlook mid-points.


LATTICE
SEMICONDUCTOR