SEMICONDUCTOR

The Low Power Programmable Leader

Second Quarter, 2020



Safe Harbor

This presentation contains forward-looking statements that involve estimates, assumptions, risks and uncertainties, including statements relating to our expectations about our systems solutions' offerings under the heading "Future".

Factors that may cause actual results to differ materially from the forward-looking statements in this presentation include those risks more fully described in Lattice's filings with the SEC including its annual report on Form 10-K for the fiscal year ended December 28, 2019 and quarterly filings.

You should not unduly rely on forward-looking statements because actual results could differ materially from those expressed in any forward-looking statements. In addition, any forward-looking statement applies only as of the date on which it is made. The Company does not intend to update or revise any forward-looking statements, whether as a result of events or circumstances after the date hereof or to reflect the occurrence of unanticipated events.



Agenda



NASDAQ: LSCC - 3



The Low Power Programmable Leader



Lattice Semiconductor Overview

APPLICATIONS & MARKETS

We enable secure control, flexible connectivity, and low power compute acceleration

WORLD CLASS SUPPLIER

In business and innovating for 35 years





Lattice Executive Leadership Team





Agenda



Lattice Value Proposition



Lattice Product Portfolio

BROAD FAMILY OF LOW POWER FPGAs

GENERAL PURPOSE



Addresses a broad range of applications across multiple markets

- Up to 150K LUTs in 10x10 mm csfBGA
- Highest density & lowest power SERDES in smallest package

FPGA FAMILIES TAILORED FOR SPECIFIC NEEDS

VIDEO CONNECTIVITY



Optimized for high speed video and sensor applications

First FPGA with hardened MIPI D-PHY
Highest performance at lowest power

ULTRA LOW POWER



ATTICE

World's lowest power FPGAs; Optimized for small form factor • Static current as low as 25 uA

 World's most popular ultra-low power FPGA

CONTROL & SECURITY

MachXO Family Solver Highe

Optimized for platform management & security applications

50% market shareHighest I/O density







LATTICE NEXUS

Next Generation FD-SOI FPGA Platform

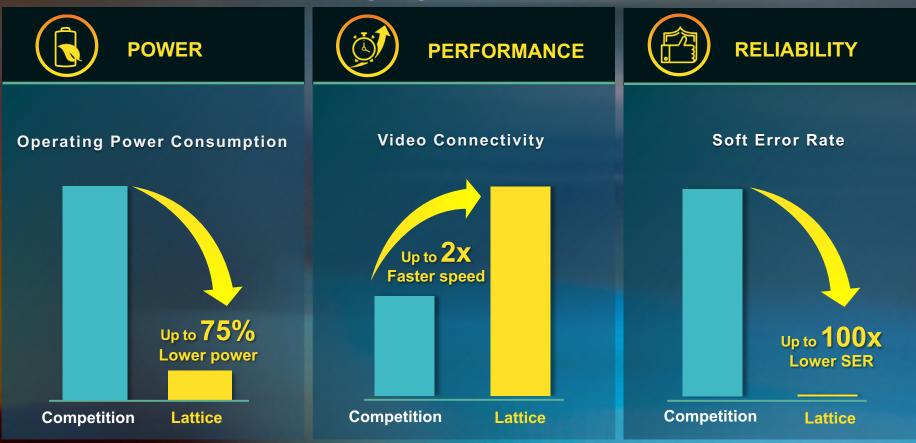
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- Low power leadership
- Edge computing ready
- Robust and reliable
- Smallest form factor
- Faster innovation cadence



AVAILABLE NOW

Lattice Nexus is Changing the FPGA Landscape





Lattice's Expanded System Solutions



Bringing Additional Value to Our Customers



New Low Power Silicon

CrossLink-NX



Embedded Vision Processing

MachXO3D



Robust Platform Security

Built on the Lattice Nexus Platform

Optimized Low Power Architecture

Available now

Secure Hardware

Designed for NIST Compliance

Customer Samples Delivered



New Value-Added Solutions and Software

Lattice mVision

mVision

Low Power Embedded Vision

Flexible Image Sensor Bridging and Aggregation

Image Processing Integration

Complete Solution Enablement



Low Power Edge AI

High Performance Inferencing Under 1W

Supports Industry Standard ML Frameworks

Complete Solution Enablement

Lattice Radiant

Advanced Ease-of-use

Simplified Flow for Faster Design Increase Re-use with IP Tools Leading Synthesis & Simulation



Agenda





Positioned in Growing End Markets



5G Wireless	Servers	Industrial IoT	ADAS	Smart Home
Switches/Routers	Client	Factory Automation	Infotainment	Wearables

Only Lattice is investing in small, low power FPGAs for these markets



Lattice Solves Communications Challenges

SCALABLE HARDWARE MANAGEMENT

my fields in my

ASIC BRIDGING

PCIe BRIDGING

CALINE

#

SECURE CONTROL

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Lattice Solves Data Center Challenges



PLATFORM FIRMWARE RESILIENCE



SYSTEM CONTROL

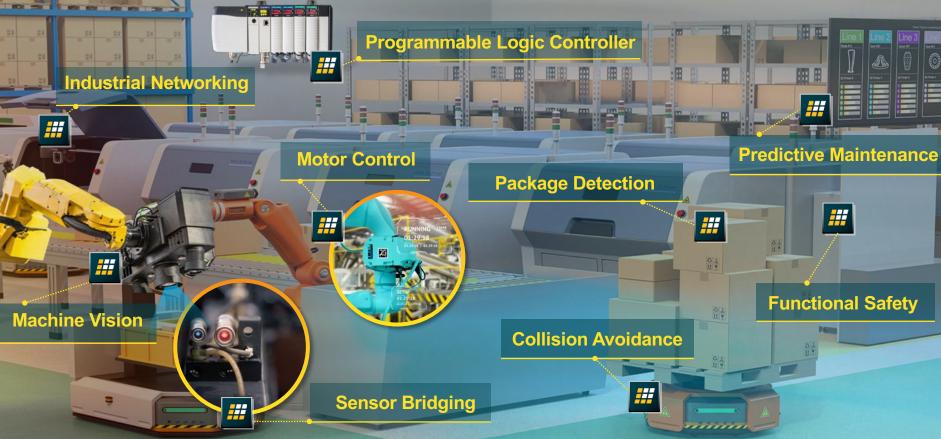
COMPUTE ACCELERATION



POWER SEQUENCING

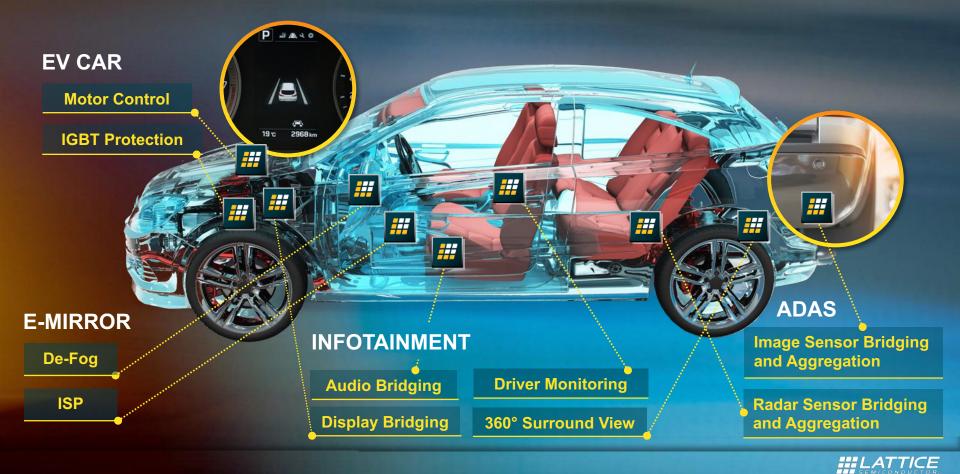


Lattice Solves Industrial Challenges

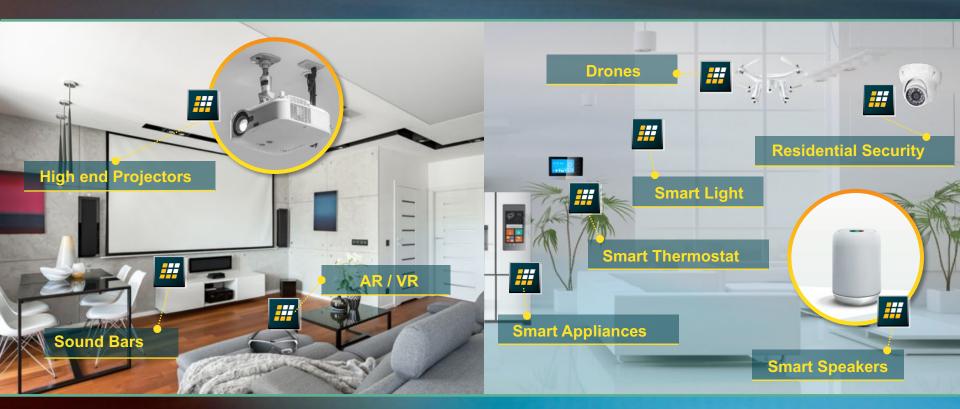




Lattice Solves Automotive Challenges



Lattice Solves Consumer Challenges









Financial Highlights





2Q19

3Q19

4Q19

Q1'20



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